

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®

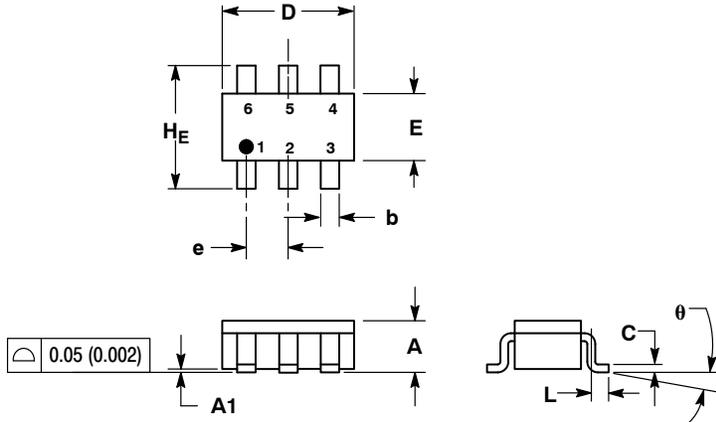


### SC-74 CASE 318F-05 ISSUE N

DATE 08 JUN 2012



SCALE 2:1

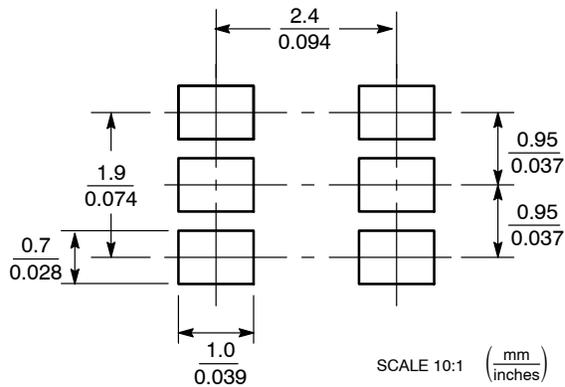


**NOTES:**

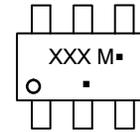
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
c	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
e	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	-	10°	0°	-	10°

**SOLDERING FOOTPRINT\***



**GENERIC MARKING DIAGRAM\***



- XXX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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|--|---|--|---|--|--|
| <p><b>STYLE 1:</b><br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. CATHODE<br/>4. CATHODE<br/>5. ANODE<br/>6. CATHODE</p>     | <p><b>STYLE 2:</b><br/>PIN 1. NO CONNECTION<br/>2. COLLECTOR<br/>3. EMITTER<br/>4. NO CONNECTION<br/>5. COLLECTOR<br/>6. BASE</p> | <p><b>STYLE 3:</b><br/>PIN 1. EMITTER 1<br/>2. BASE 1<br/>3. COLLECTOR 2<br/>4. EMITTER 2<br/>5. BASE 2<br/>6. COLLECTOR 1</p> | <p><b>STYLE 4:</b><br/>PIN 1. COLLECTOR 2<br/>2. EMITTER 1/EMITTER 2<br/>3. COLLECTOR 1<br/>4. EMITTER 3<br/>5. BASE 1/BASE 2/COLLECTOR 3<br/>6. BASE 3</p> | <p><b>STYLE 5:</b><br/>PIN 1. CHANNEL 1<br/>2. ANODE<br/>3. CHANNEL 2<br/>4. CHANNEL 3<br/>5. CATHODE<br/>6. CHANNEL 4</p> | <p><b>STYLE 6:</b><br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. CATHODE<br/>4. CATHODE<br/>5. CATHODE<br/>6. CATHODE</p> |
| <p><b>STYLE 7:</b><br/>PIN 1. SOURCE 1<br/>2. GATE 1<br/>3. DRAIN 2<br/>4. SOURCE 2<br/>5. GATE 2<br/>6. DRAIN 1</p> | <p><b>STYLE 8:</b><br/>PIN 1. EMITTER 1<br/>2. BASE 2<br/>3. COLLECTOR 2<br/>4. EMITTER 2<br/>5. BASE 1<br/>6. COLLECTOR 1</p>    | <p><b>STYLE 9:</b><br/>PIN 1. EMITTER 2<br/>2. BASE 2<br/>3. COLLECTOR 1<br/>4. EMITTER 1<br/>5. BASE 1<br/>6. COLLECTOR 2</p> | <p><b>STYLE 10:</b><br/>PIN 1. ANODE/CATHODE<br/>2. BASE<br/>3. EMITTER<br/>4. COLLECTOR<br/>5. ANODE<br/>6. CATHODE</p>                                    | <p><b>STYLE 11:</b><br/>PIN 1. EMITTER<br/>2. BASE<br/>3. ANODE/CATHODE<br/>4. ANODE<br/>5. CATHODE<br/>6. COLLECTOR</p>   |  |

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<b>STATUS:</b>	<b>ON SEMICONDUCTOR STANDARD</b>	
<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>SC-74</b>	<b>PAGE 1 OF 2</b>

