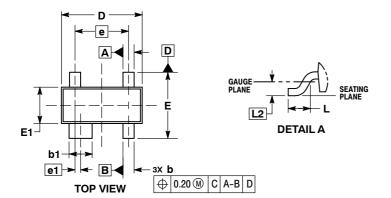
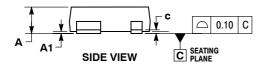
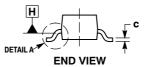


SOT-143 CASE 318A-06 **ISSUE U** 

**DATE 07 SEP 2011** 





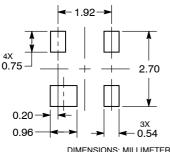


## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIM-UM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, AND GATE BURRS SHALL NOT EXCEED 0.25 PER SIDE.
  DIMENSION E1 DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH AND PROTRUSION SHALL NOT EXCEED 0.25 PER SIDE.
- 5. DIMENSIONS D AND E1 ARE DETERMINED AT DATUM H.
  6. DATUMS A AND B ARE DETERMINED AT DATUM H.

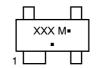
	MILLIMETERS	
DIM	MIN	MAX
Α	0.80	1.12
A1	0.01	0.15
b	0.30	0.51
b1	0.76	0.94
C	0.08	0.20
D	2.80	3.05
Е	2.10	2.64
E1	1.20	1.40
е	1.92 BSC	
e1	0.20 BSC	
L	0.35	0.70
L2	0.25 BSC	

## **RECOMMENDED SOLDERING FOOTPRINT**



		, 0
		]
0.20→	1' 1'	av
0.96	<b>-</b>	3X <b>⋖</b> − 0.54
С	IMENSION	S: MILLIMETERS

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

> STYLE 6: PIN 1. GND 2. RF IN 3. VREG 4. RF OUT

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. COLLECTOR	PIN 1. SOURCE	PIN 1. GROUND	PIN 1. OUTPUT	PIN 1. SOURCE
2. EMITTER	2. DRAIN	2. SOURCE	2. GROUND	2. DRAIN
3. EMITTER	3. GATE 1	3. INPUT	3. GROUND	3. GATE 1
4. BASE	4. GATE 2	4. OUTPUT	4. INPUT	4. SOURCE
STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. SOURCE	PIN 1. SOURCE	PIN 1. GND	PIN 1. DRAIN	PIN 1. SOURCE
2. GATE	2. GATE	2. IOUT	2. N/C	2. GATE 1
3. DRAIN	3. DRAIN	3. VCC	3. SOURCE	3. GATE 2
4. SOURCE	4. N/C	4. VREF	4. GATE	4. DRAIN

DOCUMENT NUMBER:	98ASB42227B	Electronic versions are uncontrolled e	
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document Repository. Print versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
NEW STANDARD:			
DESCRIPTION:	SOT-143	PA	AGE 1 OF 2

ON	Semiconductor®	ON

DOCUMENT NUMBER: 98ASB42227B

PAGE 2 OF 2

IOOUE	DEV/OLON	DATE
ISSUE	REVISION	DATE
U	REDREW TO JEDEC STANDARDS. ADDED SOLDER FOOTPRINT. REQ. BY D. TRUHITTE.	07 SEP 2011

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.