



ON Semiconductor

DN05100/D

Design Note – DN05100/D

# 45W TYPE-C PD2.0 Power Adapter Solution

Device	Application	Input Voltage	Output Power	Topology	I/O Isolation
NCP1340B3	Smart phone, PAD and NB adapter supporting PD2.0 and QC2.0	90Vac to 264Vac	45W	Flyback	Isolated (3 kV)
NCP43080D					
NTMFS6B03					
ATP104					

	PD Output Specification	QC Output Specification
<b>Output Voltage</b>	5V, 9V, 12V, 15V, 20V	5V, 9V, 12V
<b>Nominal Current</b>	5V/3A, 9V/3A, 12V/3A, 15V/3A, 20V/2.25A	5V/3A, 9V/3A, 12V/3A
<b>Max Current</b>	5V/3A, 9V/3A, 12V/3A, 15V/3A, 20V/2.25A	5V/3A, 9V/3A, 12V/3A
<b>Min Current</b>	zero	zero

<b>Avg. Efficiency</b>	>90%@20V2.25A at board end, 115&230Vac
<b>Ripple</b>	<100mV
<b>Standby Power</b>	<30mW @5V&230Vac(No cable plug in)
<b>Power Density</b>	1.15W/cm^3
<b>Protection</b>	Adaptive UVP, OVP, SCP, OTP
<b>Size</b>	57mmx36mmx19mm

## Circuit Description

This design note describes a 45 watt, Type C interface PD2.0, universal AC input, constant voltage power supply intended for smart phone, PAD and NB adaptor supporting PD2.0 or QC2.0 protocol, where isolation from the AC mains is required, and low cost, high efficiency, and low standby power are essential.

The featured power supply is a simple QR flyback topology utilizing ON Semiconductor's NCP1340B3 HF PWM controller, NCP43080D synchronous rectified controller, NTMFS6B03 synchronous MOSFET and ATP104 Switch MOSFET. This Design Note provides the complete circuit schematic details, PCB and BOM for 45W Type C Interface PD2.0 Power adapter solution which supports PD output (5V/3A, 9V/3A, 12V/3A, 15V/3A, 20V/2.25A).

This design combined with CanYon's CY2211 PD2.0 protocol controller to provide PD2.0 and QC2.0 functions. This design also proposes a dual auxiliary power supply to supply PWM

controller, the PWM controller is supplied by high voltage auxiliary voltage at low output voltage and supplied by low voltage auxiliary voltage at high output voltage and also shuts down zener bias of high voltage Vcc while low voltage auxiliary voltage supplies controller.

This design also uses synchronous rectified controller to provide high efficiency and also uses an external Vcc pulse regulator to supply synchronous controller to ensure controller can work below 4v.

## Key Features

- Universal AC input range (90 – 264 Vac)
- Very low standby (5V & 230Vac) power consumption with no cable plug in
- Very low ripple and noise
- Inherent SCP and OCP protection
- High operation frequency up to 150kHz
- High power density ( $1.15 \text{ W/cm}^3$ )
- Quick switching off FET while unplugging cable and switching on FET at Vbus dropping to 5v while plugging cable again
- Quasi-Resonant current mode control with Valley Switching
- Valley lockout avoids audible noise at valley jumping operation
- Support TYPE-C PD2.0&QC2.0 protocol
- Adaptive Output OVP and UVP
- Open loop protection
- Board size: 57mmx36mmx19mm

## Block Diagram and BOARD Photos

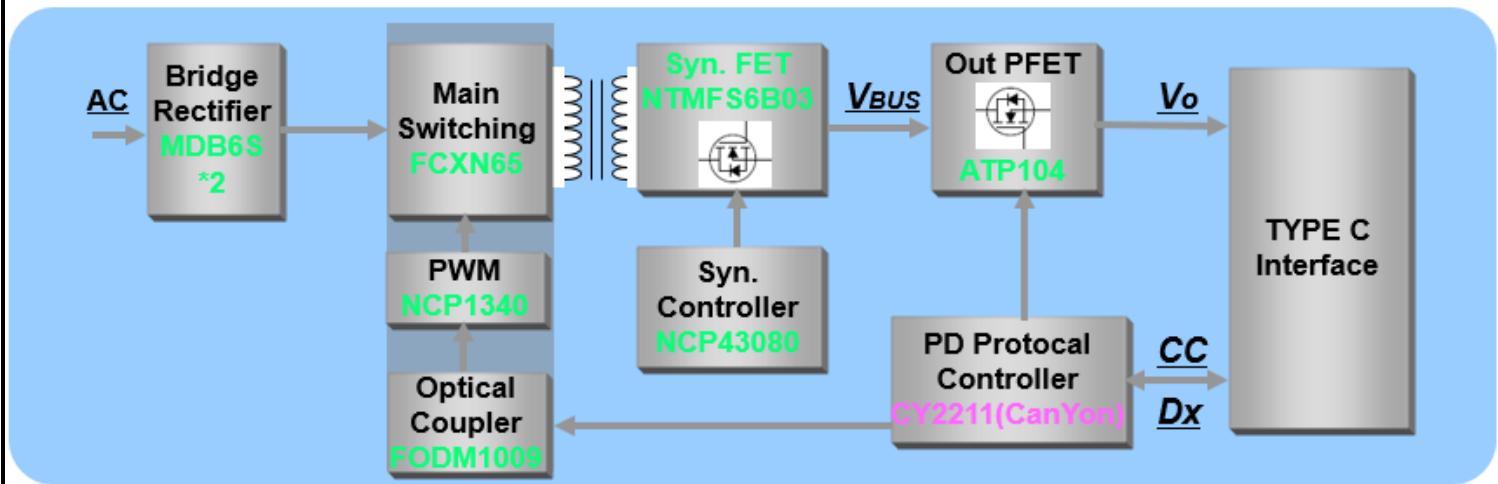


Figure 1, Overall cycle of 45W TYPE-C PD adapter Solution

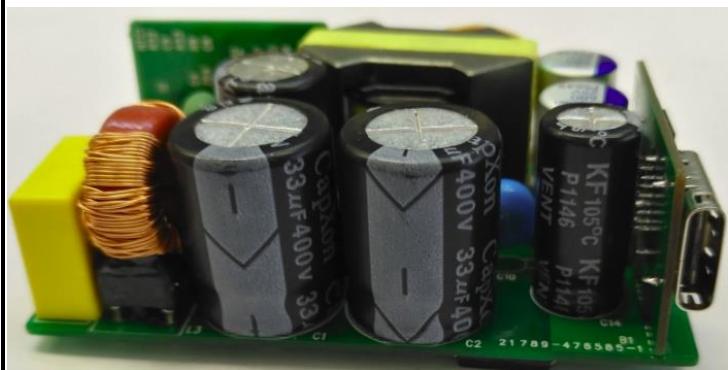


Figure 2, Side view 1 of demoboard

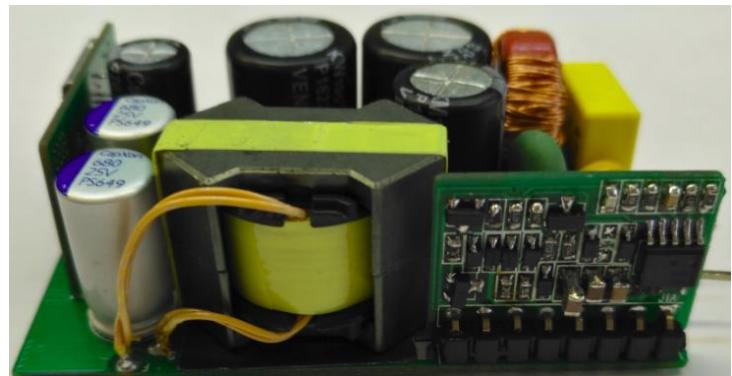
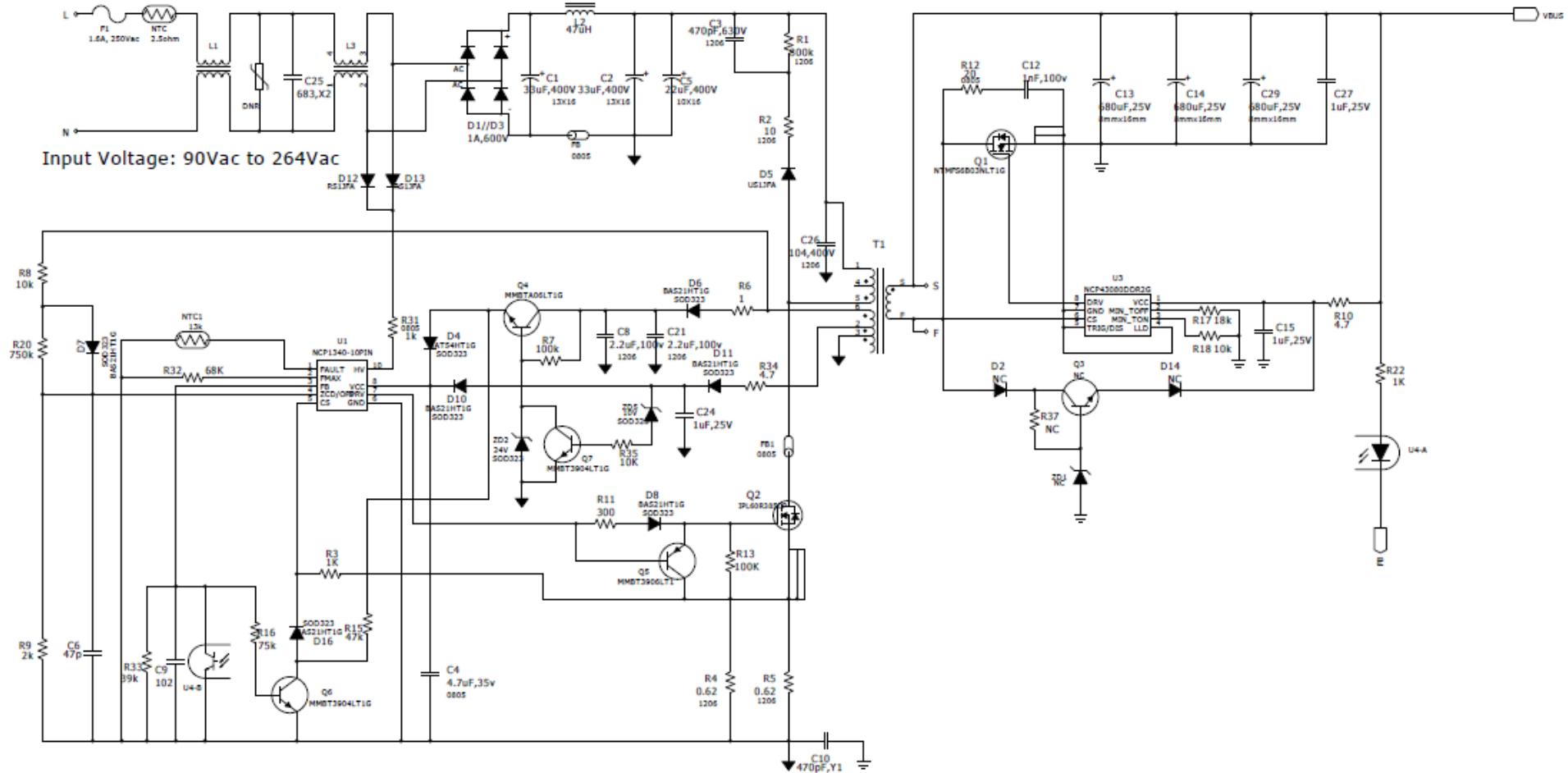
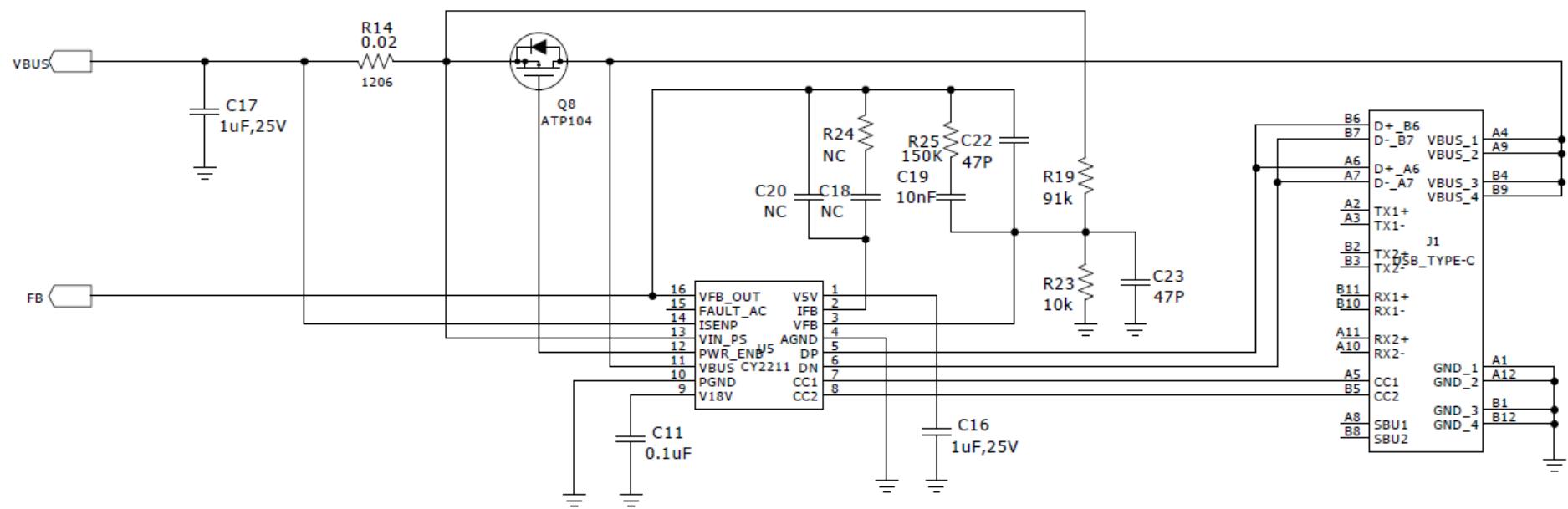


Figure 3, Side view 2 of demoboard

# DN05100/D Circuit Schematic



**DN05100/D**  
**Circuit Schematic (Continued)**



**DN05100/D  
PCB**

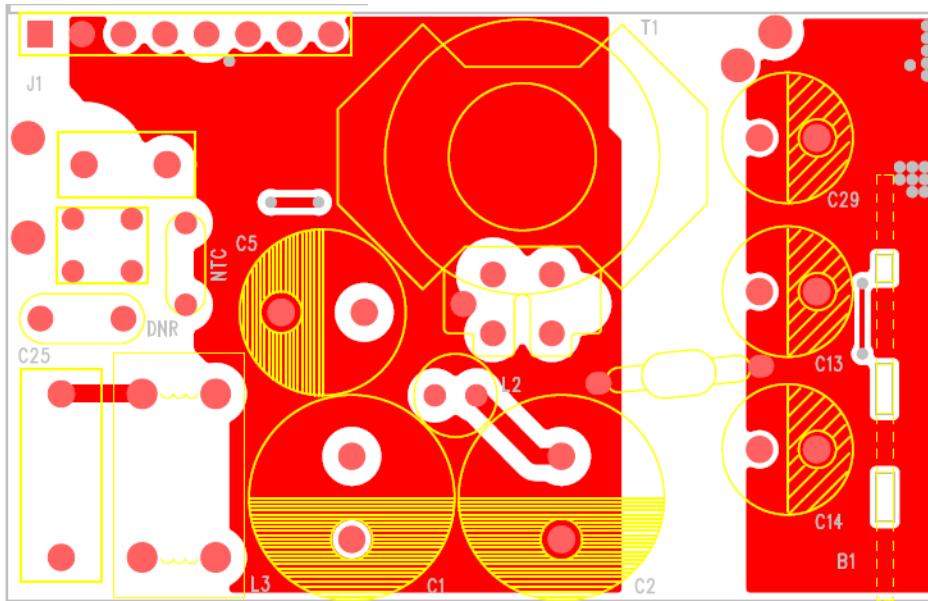


Figure 3, Top View of Mainboard's PCB

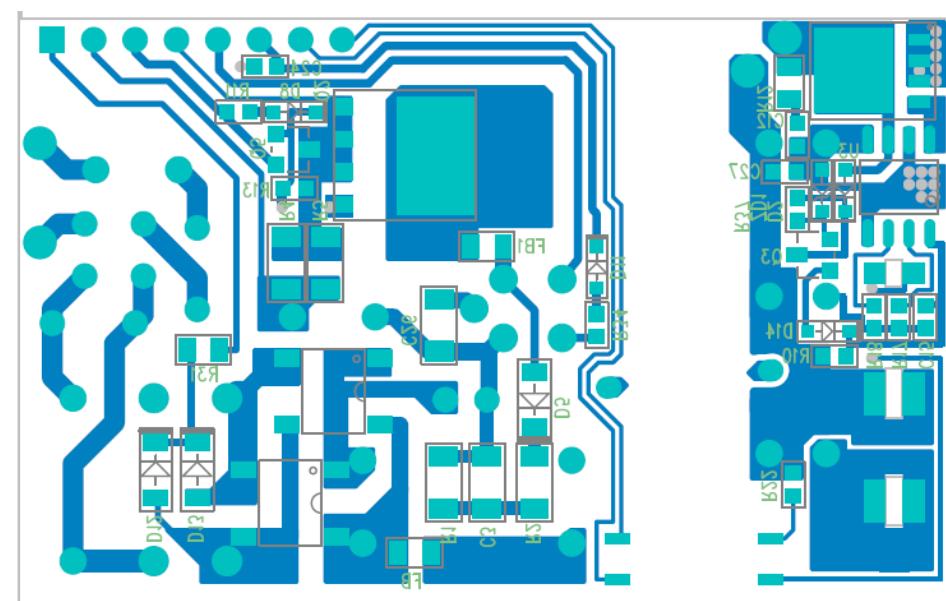


Figure 4, Bottom View of Mainboard's PCB

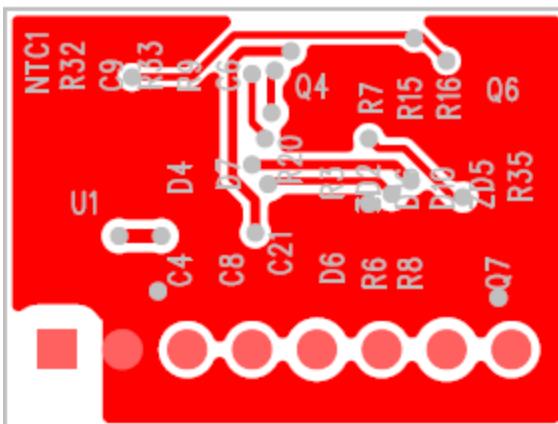


Figure 5, Top View of PWM control board's PCB

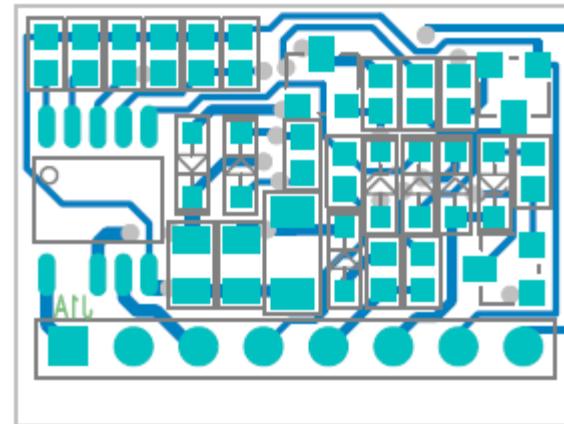


Figure 6, Bottom View of PWM control board's PCB

**DN05100/D  
PCB (Continued)**

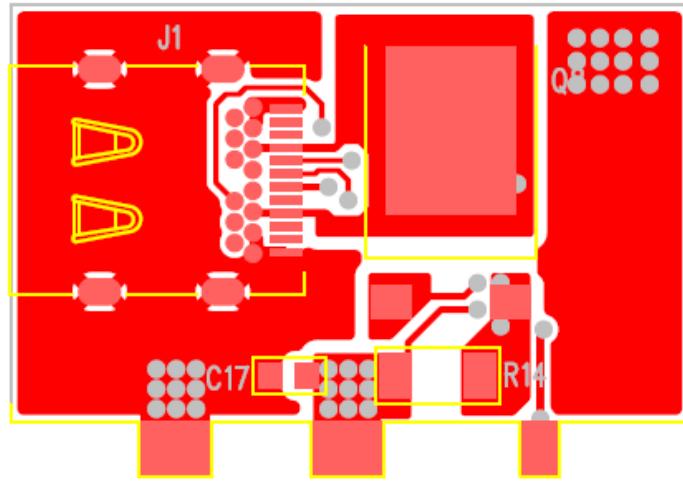


Figure 7, Top View of PD control board (CY2211)'s PCB

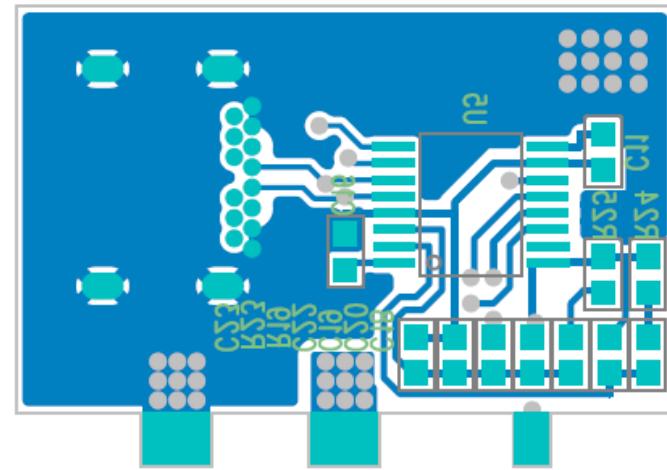
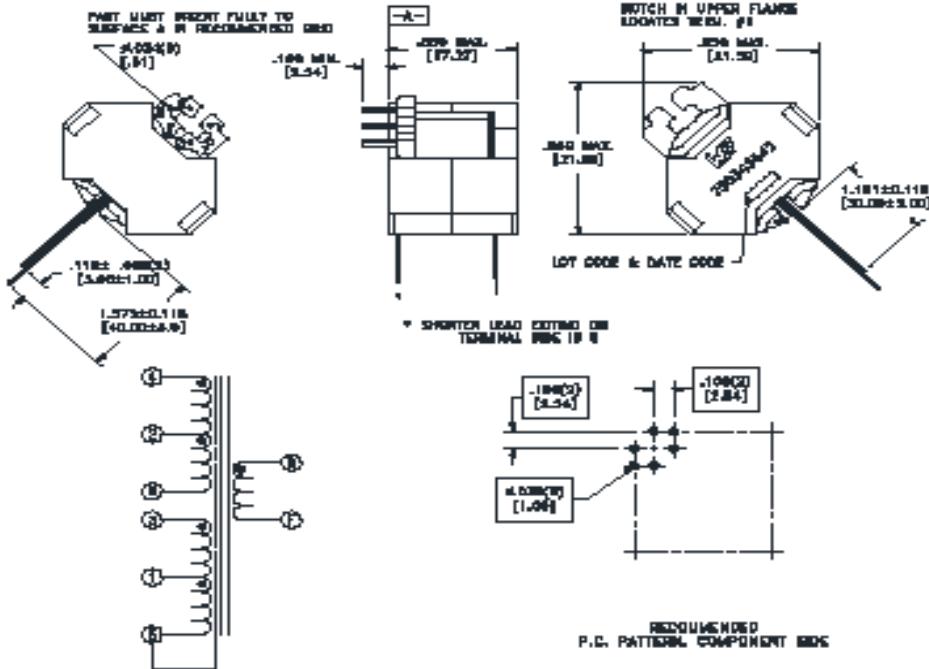


Figure 8, Bottom View of PD control board (CY2211)'s PCB

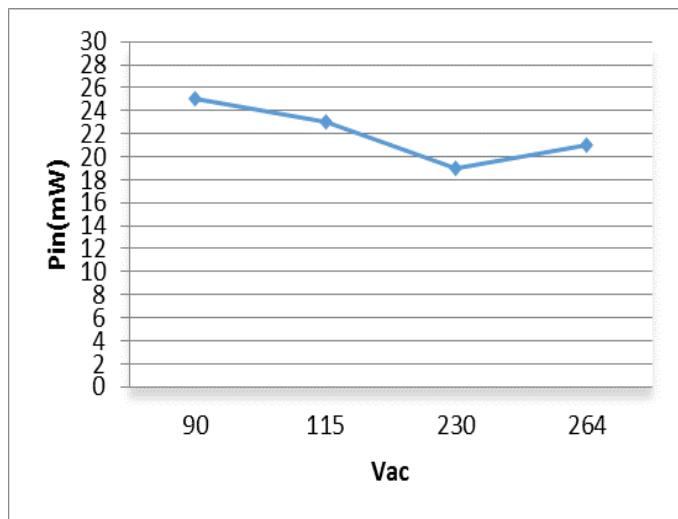
# DN05100/D

## T1 Transformer Designs (Available from Wurth Electronics)

CUSTOMER TERMINAL	RoHS	LEAD(Pb)-FREE																																					
Sn 96%, Ag 4%	Yes	Yes	 more than you expect.																																				
			<b>ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:</b> <table border="1" style="width: 100%; border-collapse: collapse; font-size: small;"> <thead> <tr> <th style="width: 30%;">PARAMETER</th> <th style="width: 30%;">TEST CONDITIONS</th> <th style="width: 40%;">VALUE</th> </tr> </thead> <tbody> <tr> <td>D.C. RESISTANCE S-F</td> <td>(@20°C)</td> <td>0.020 ohms max.</td> </tr> <tr> <td>D.C. RESISTANCE 3-1</td> <td>(@20°C)</td> <td>0.325 ohms max.</td> </tr> <tr> <td>D.C. RESISTANCE 1-5</td> <td>(@20°C)</td> <td>0.220 ohms max.</td> </tr> <tr> <td>D.C. RESISTANCE 4-6</td> <td>(@20°C)</td> <td>0.585 ohms max.</td> </tr> <tr> <td>INDUCTANCE 4-6</td> <td>10kHz, 1V, L<sub>s</sub></td> <td>360.00μH ±10%</td> </tr> <tr> <td>LEAKAGE INDUCTANCE 4-6</td> <td>tie(1+3+5+S+F), 100kHz, 100mV, L<sub>s</sub></td> <td>6μH max.</td> </tr> <tr> <td>DIELECTRIC 1-S</td> <td>tie(1+6), 3750VAC, 1 second</td> <td>3000VAC, 1 minute</td> </tr> <tr> <td>DIELECTRIC S-CORE</td> <td>3750VAC, 1 second</td> <td>3000VAC, 1 minute</td> </tr> <tr> <td>TURNS RATIO (4-6):(3-1)</td> <td></td> <td>4.33:1, ±2%</td> </tr> <tr> <td>TURNS RATIO (4-6):(1-5)</td> <td></td> <td>6.5:1, ±2%</td> </tr> <tr> <td>TURNS RATIO (4-6):(S-F)</td> <td></td> <td>6.5:1, ±2%</td> </tr> </tbody> </table>	PARAMETER	TEST CONDITIONS	VALUE	D.C. RESISTANCE S-F	(@20°C)	0.020 ohms max.	D.C. RESISTANCE 3-1	(@20°C)	0.325 ohms max.	D.C. RESISTANCE 1-5	(@20°C)	0.220 ohms max.	D.C. RESISTANCE 4-6	(@20°C)	0.585 ohms max.	INDUCTANCE 4-6	10kHz, 1V, L <sub>s</sub>	360.00μH ±10%	LEAKAGE INDUCTANCE 4-6	tie(1+3+5+S+F), 100kHz, 100mV, L <sub>s</sub>	6μH max.	DIELECTRIC 1-S	tie(1+6), 3750VAC, 1 second	3000VAC, 1 minute	DIELECTRIC S-CORE	3750VAC, 1 second	3000VAC, 1 minute	TURNS RATIO (4-6):(3-1)		4.33:1, ±2%	TURNS RATIO (4-6):(1-5)		6.5:1, ±2%	TURNS RATIO (4-6):(S-F)		6.5:1, ±2%
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<b>GENERAL SPECIFICATIONS:</b> OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise. Designed to comply with the following requirements as defined by IEC60065-1, EN60065-1, UL60065-1/CSA60065-1 and AS/NZS60065.1: - Reinforced insulation for a primary circuit at a working voltage of 265Vrms, 400Vpeak, Overvoltage Category II.			<b>DFM</b> Packaging Specifications <b>DATE</b> Method: Tray <b>ENG</b> NWA PKG-TBD <b>REV.</b> 00 <b>DATE</b> 4/11/2017																																				
Wire Insulation & RoHS status not affected by wire color. Wire Insulation color may vary depending on availability. Tolerances unless otherwise specified: Angles: ±1° Decimals: ±.005 [.13] Fractions: ±1/64 Footprint: ±.001 [.03]			<b>DRAWING TITLE</b> <b>PART NO.</b> <b>TRANSFORMER</b> <b>750343542</b> <b>SPECIFICATION SHEET 1 OF 1</b>																																				

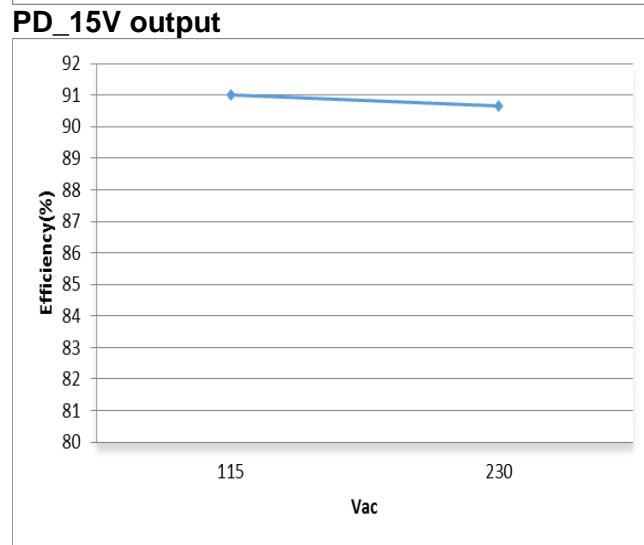
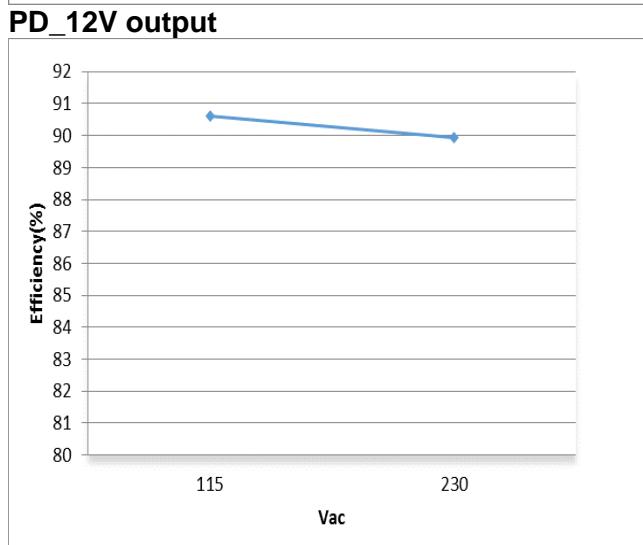
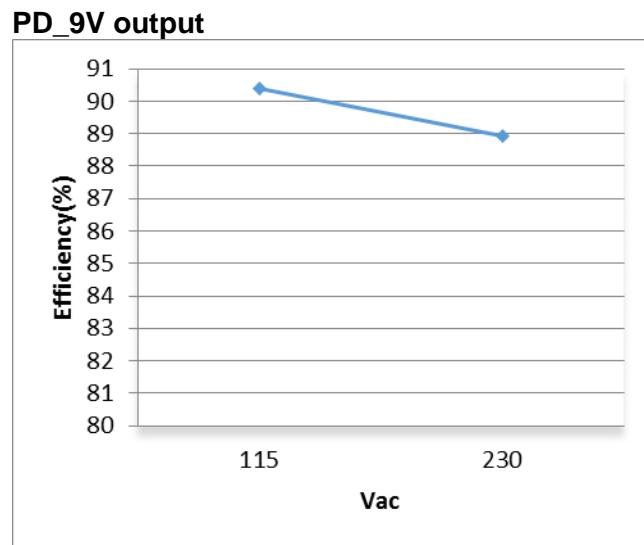
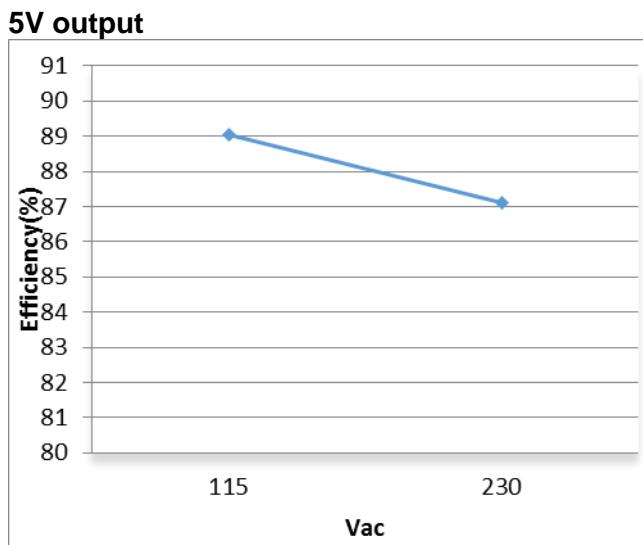
# Standby Power at 5V Output (Cable unplug) @ 90 Vac to 264 Vac Input

Test condition: all efficiency are tested at board end



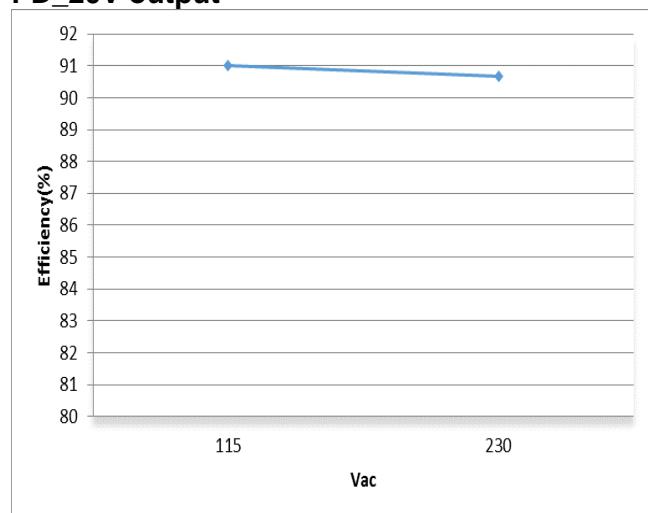
## Average Efficiency @ 115 Vac & 230 Vac Input

Test condition: all efficiency are tested at board end

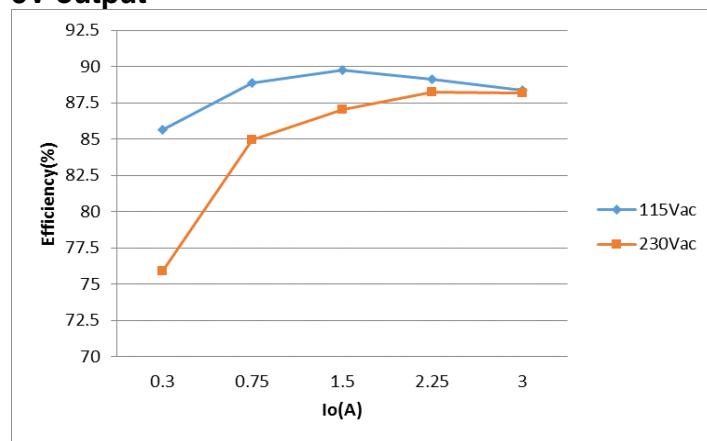
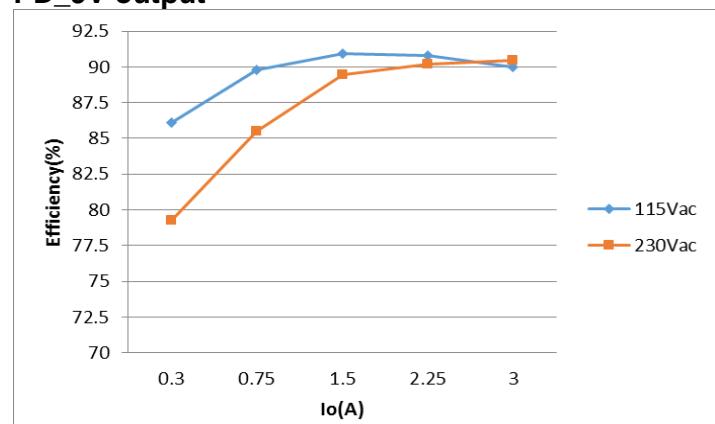
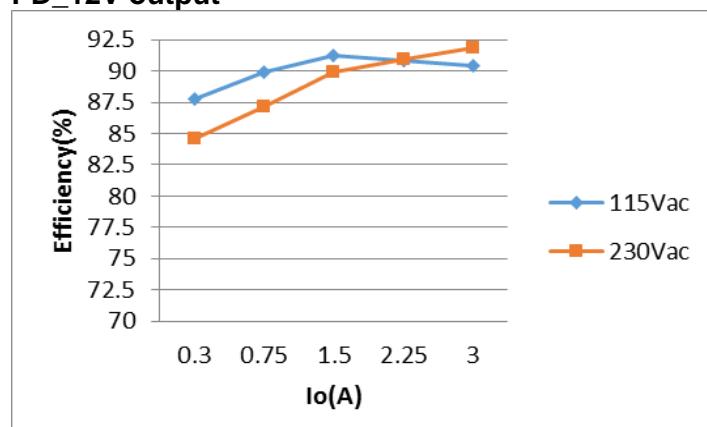
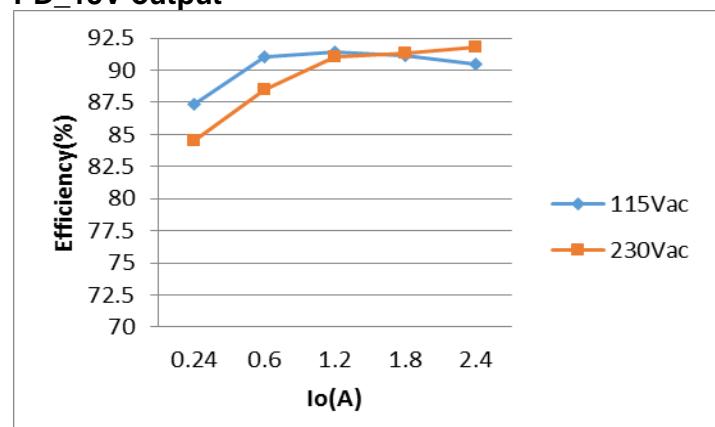


**Average Efficiency @ 115 Vac & 230 Vac Input (Continued)**

Test condition: all efficiency are tested at board end

**PD\_20V output****Efficiency vs Output Load Curves @ 115 Vac & 230 Vac Input**

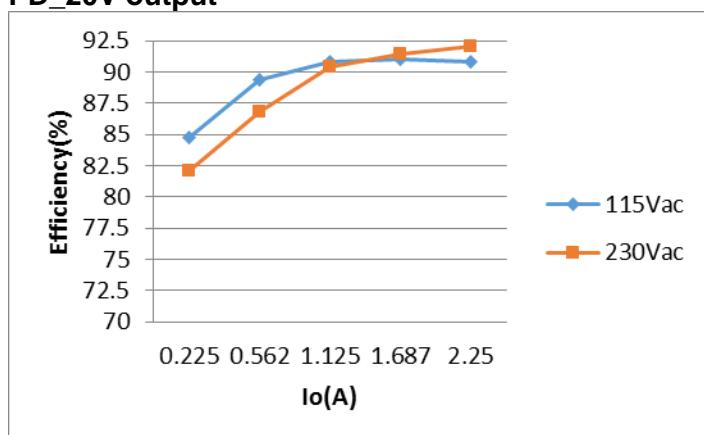
Test condition: all efficiency are tested at board end

**5V output****PD\_9V output****PD\_12V output****PD\_15V output**

# Efficiency vs Output Load Curves @ 115 Vac & 230 Vac Input (Continued)

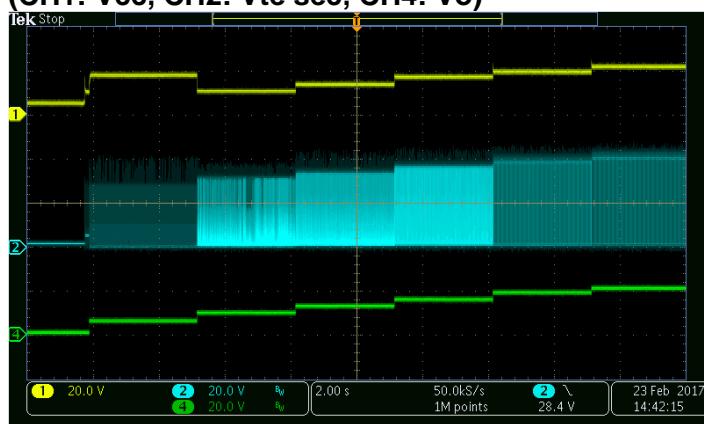
Test condition: all efficiency are tested at board end

## PD\_20V output



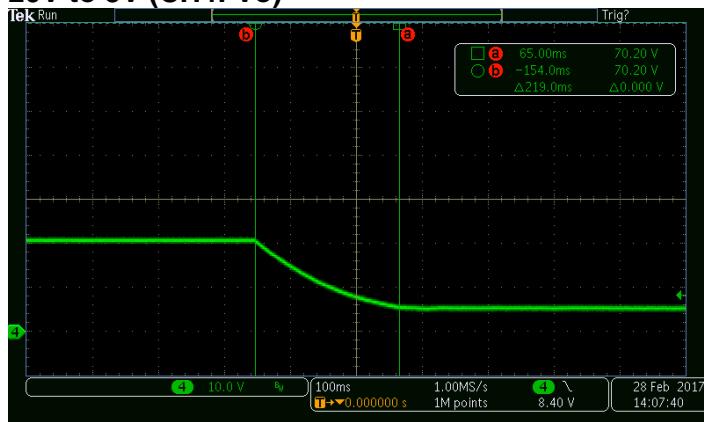
## Power On and PD Voltage Change (5V > 9V > 12V > 15V > 18V >20V)

(CH1: Vcc, CH2: Vte sec, CH4: Vo)



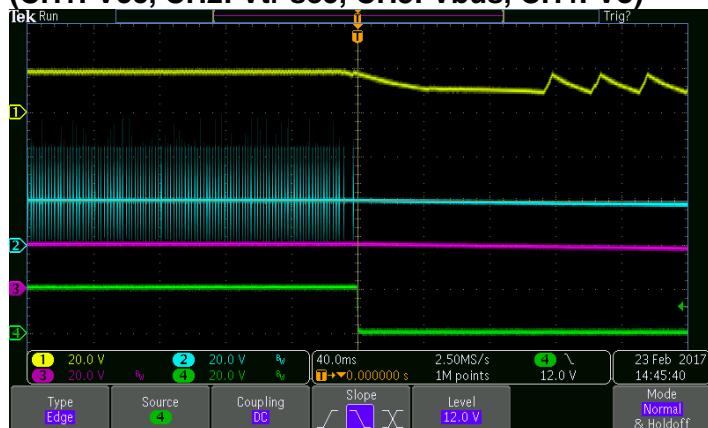
## PD Transition with PD Emulator

20V to 5V (CH4: Vo)



## Discharge Time @ Unplug cable

**PD (20V to 5V)**  
**(CH1: Vcc, CH2: Vtr sec, CH3: Vbus, CH4: Vo)**



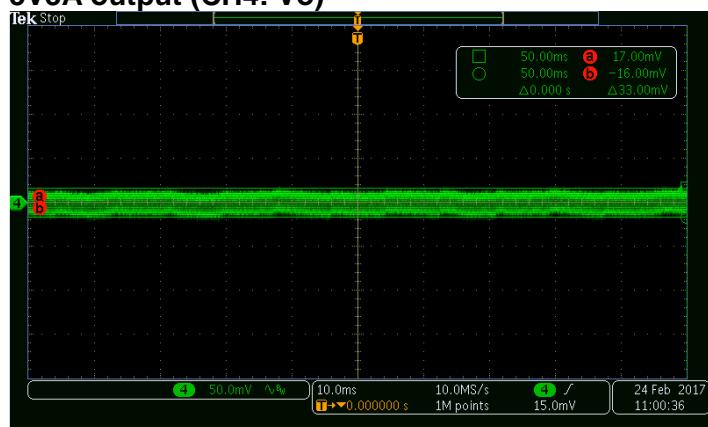
## Quick Unplug/Plug Cable

**CH3: Vbus, CH4:Vo**

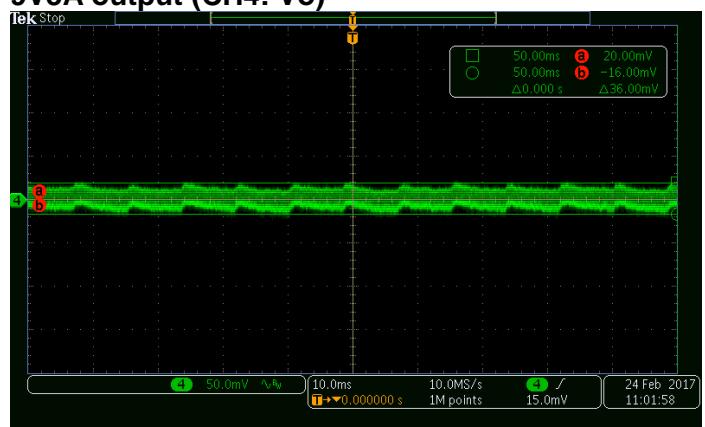


## Output Ripple @ 90 Vac Input, 3A Output

**5V3A output (CH4: Vo)**

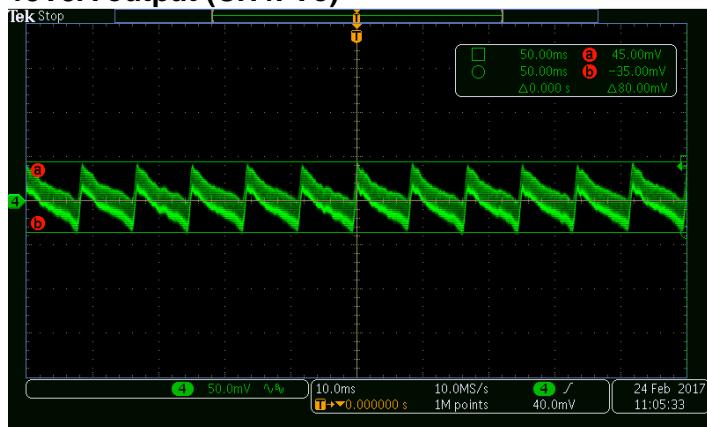


**9V3A output (CH4: Vo)**

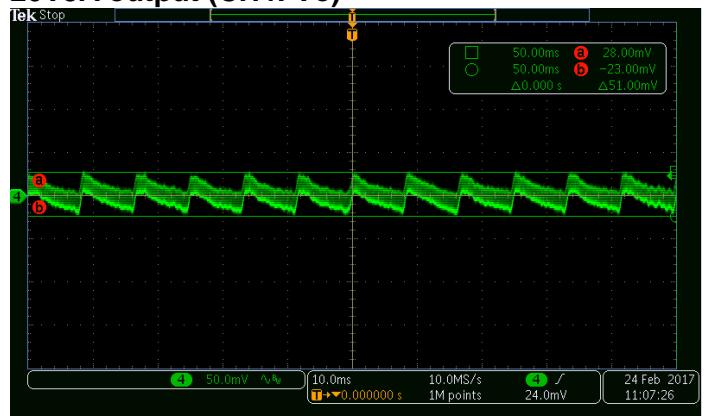


# Output Ripple @ 90 Vac Input, 3A Output (Continued)

## 15V3A output (CH4: Vo)

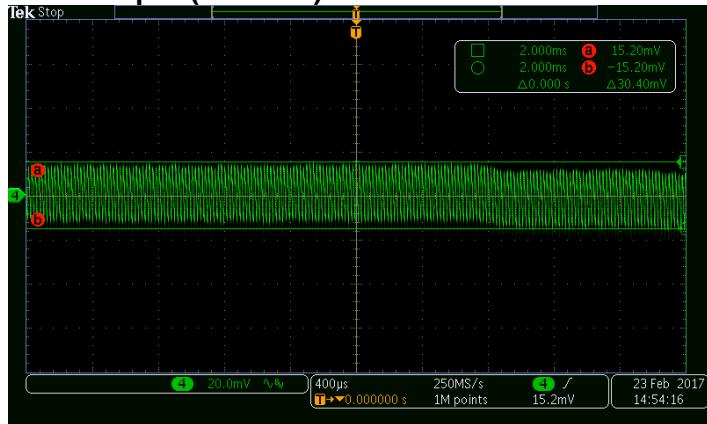


## 20V3A output (CH4: Vo)

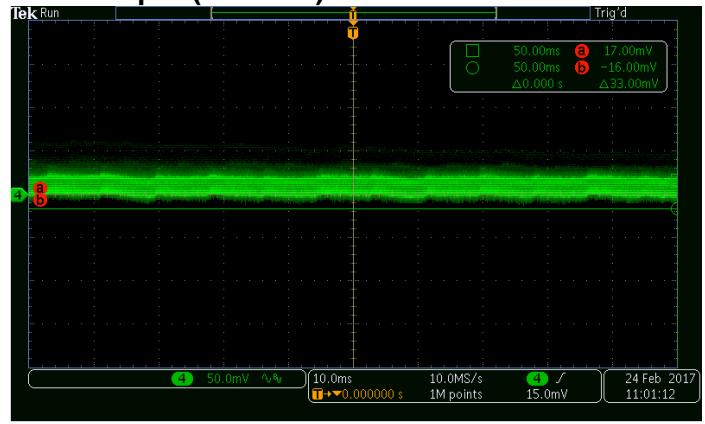


# Output Ripple @ 115 Vac Input, 3A Output

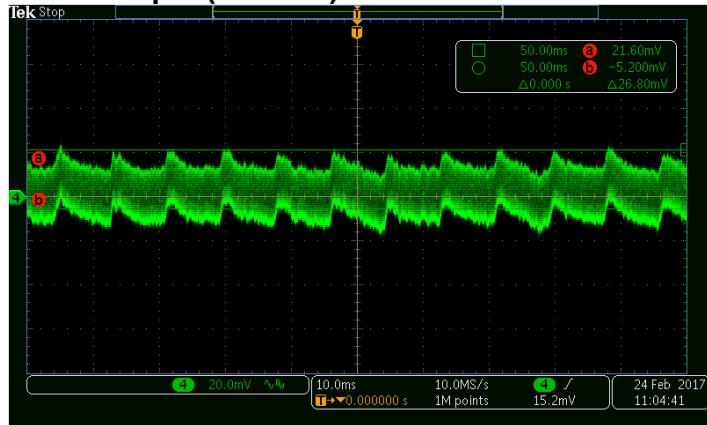
## 5V3A output (CH4: Vo)



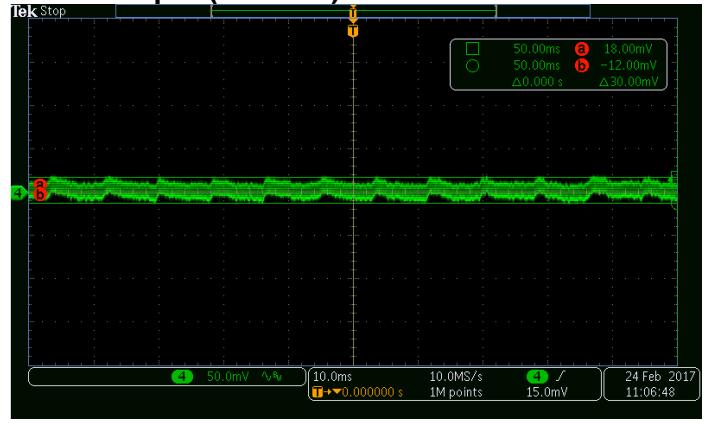
## 9V3A output (CH4: Vo)



## 15V3A output (CH4: Vo)

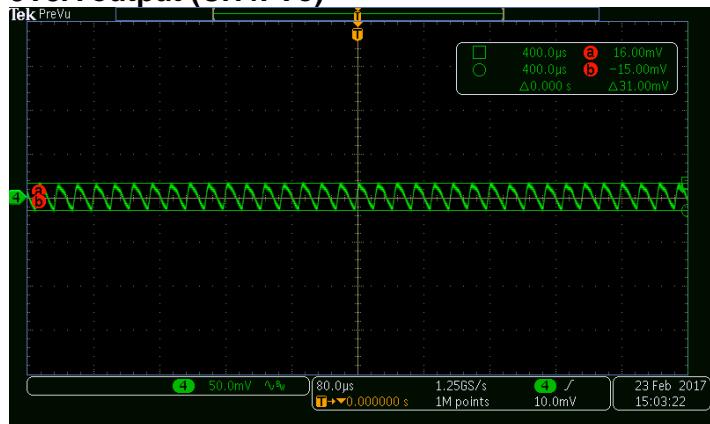


## 20V3A output (CH4: Vo)

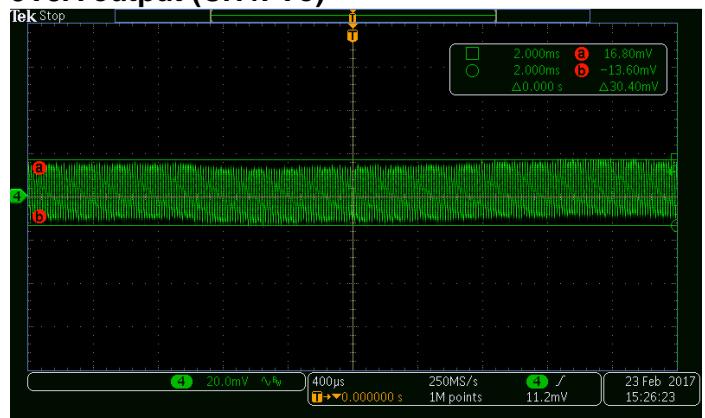


## Output Ripple @ 230 Vac Input, 3A Output

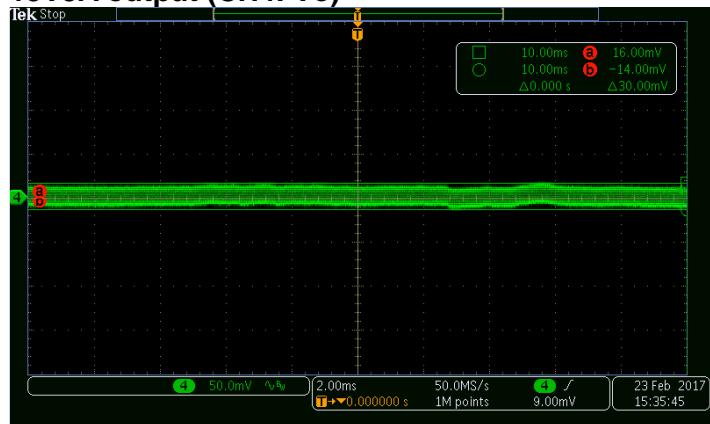
**5V3A output (CH4: Vo)**



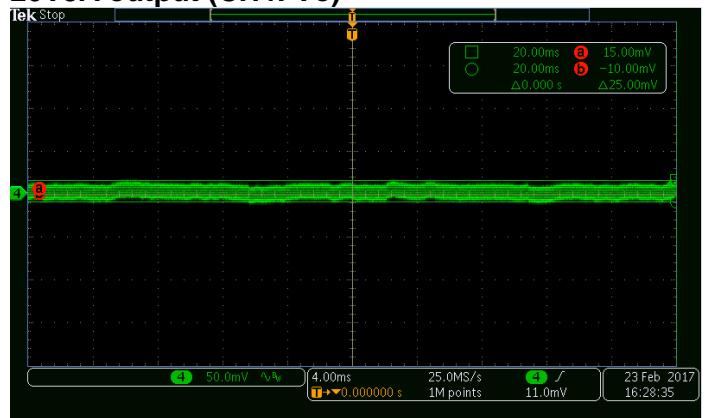
**9V3A output (CH4: Vo)**



**15V3A output (CH4: Vo)**

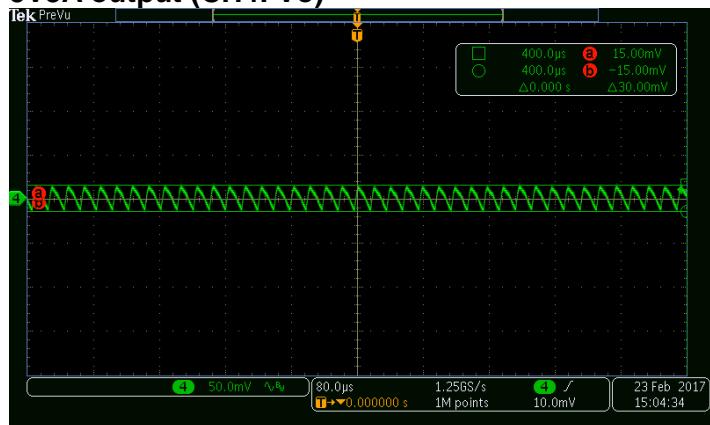


**20V3A output (CH4: Vo)**

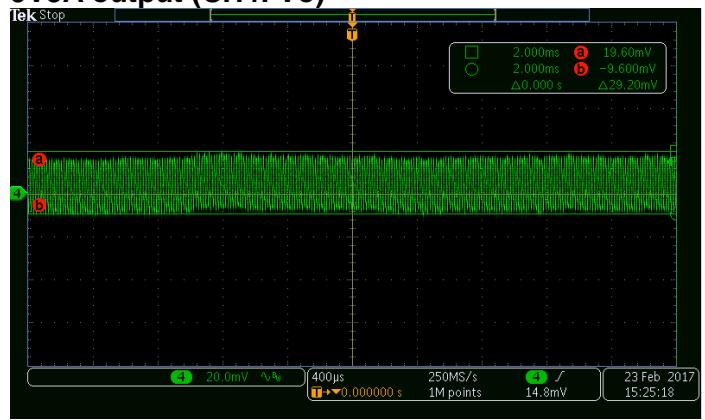


## Output Ripple @ 264 Vac Input, 3A Output

**5V3A output (CH4: Vo)**

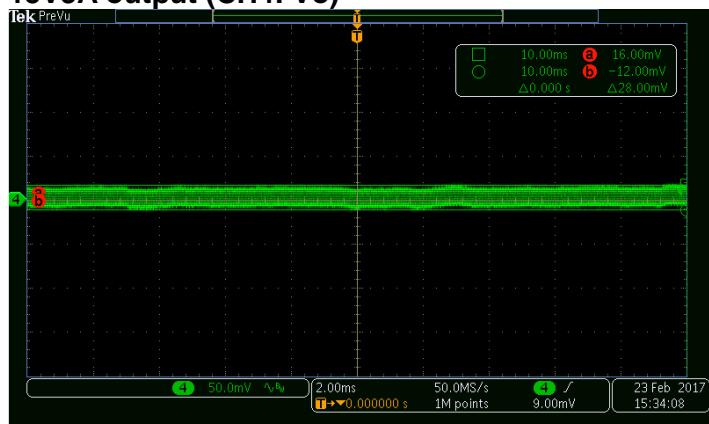


**9V3A output (CH4: Vo)**

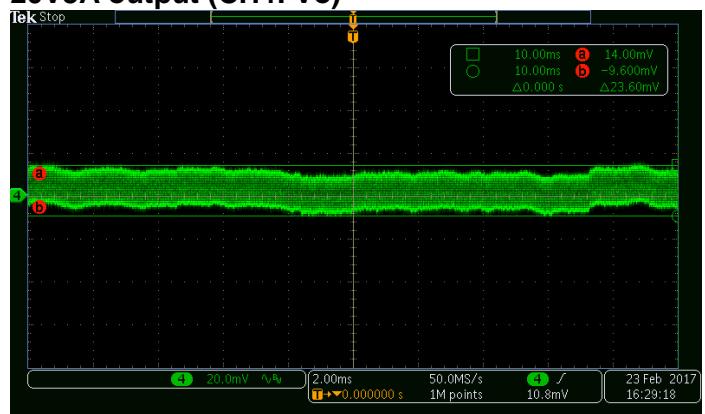


## Output Ripple @ 264 Vac Input, 3A Output (Continued)

15V3A output (CH4: Vo)

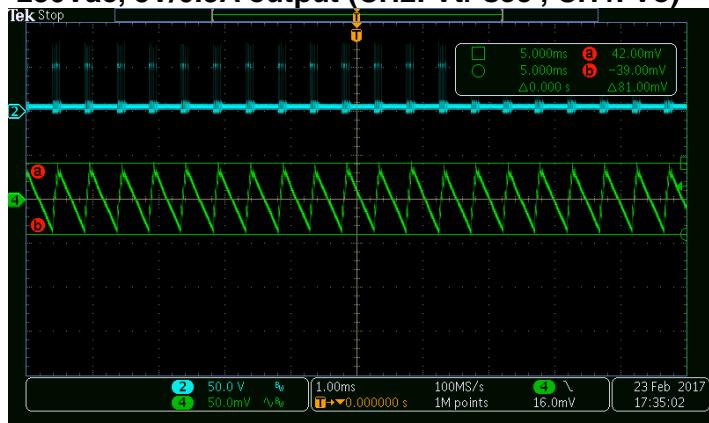


20V3A output (CH4: Vo)

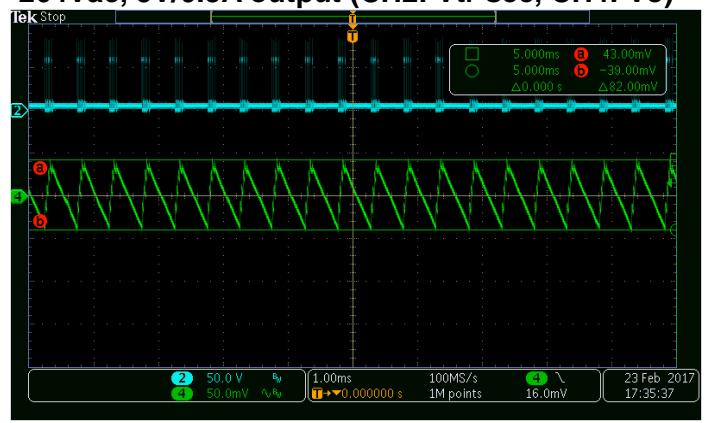


## Output Ripple @ High Line & Light Load

230Vac, 5V/0.3A output (CH2: Vtr sec , CH4: Vo)



264Vac, 5V/0.3A output (CH2: Vtr sec, CH4: Vo)



264Vac, 15V/0.15A output (CH2: Vtr sec, CH4: Vo)



264Vac, 20V/0.1A output (CH2: Vtr sec, CH4: Vo)



# Dynamic Test @ 115 Vac Input

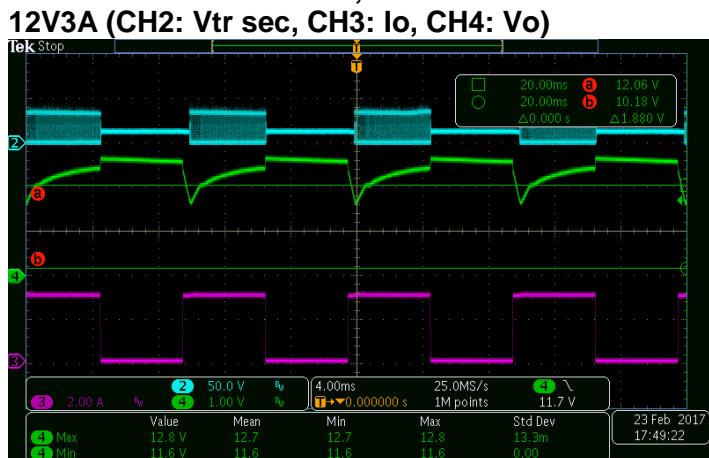
**5V3A (CH2: Vtr sec, CH3: Io, CH4: Vo)**



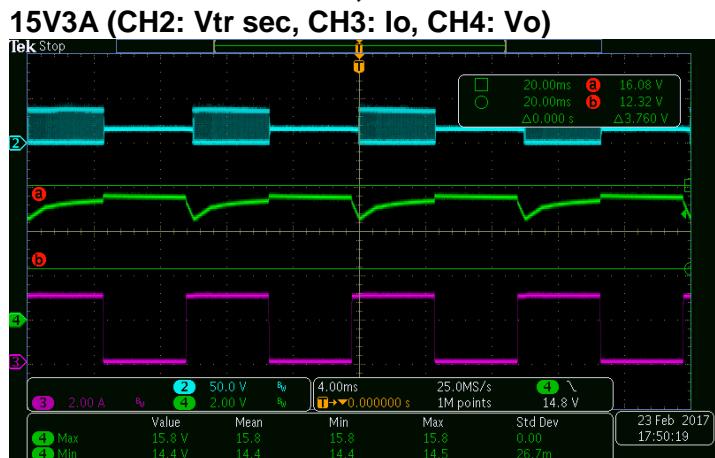
**9V3A (CH2: Vtr sec, CH3: Io, CH4: Vo)**



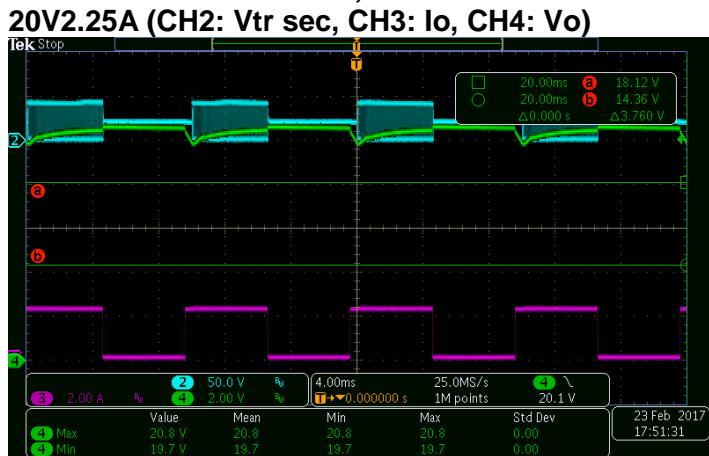
**12V3A (CH2: Vtr sec, CH3: Io, CH4: Vo)**



**15V3A (CH2: Vtr sec, CH3: Io, CH4: Vo)**



**20V2.25A (CH2: Vtr sec, CH3: Io, CH4: Vo)**



**Test condition: 0-2.25A, 10mS cycle, 125mA/us  
1m cable, tested at E-load**

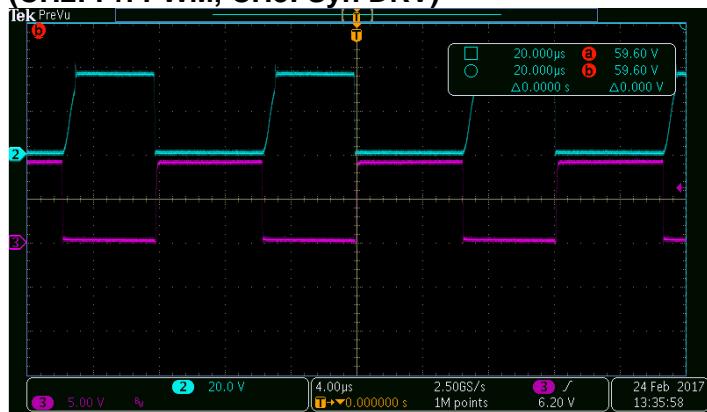
# OCP @ 264 Vac Input, 9 Vdc Output

(CH1: Vdrain, CH2: Vtr sec, CH3: Io, CH4: Vo)

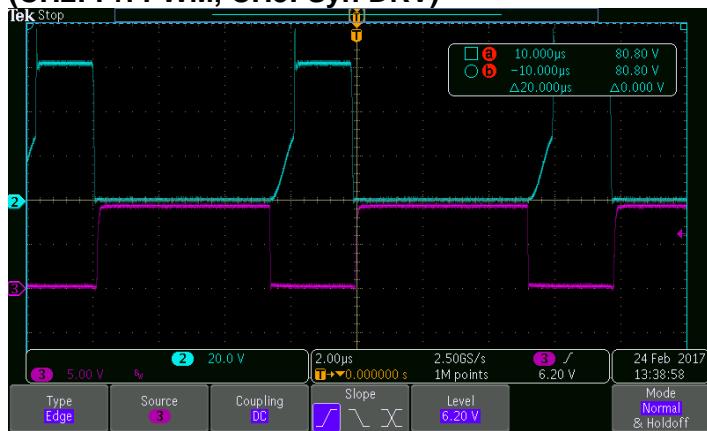


## Synchronous Drive

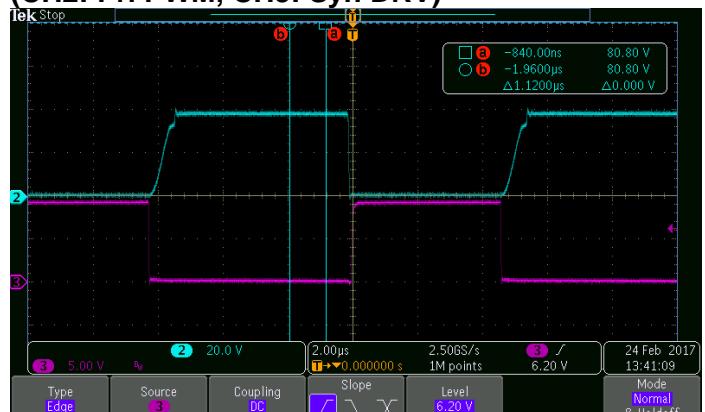
115 Vac input, 15V3A output  
(CH2: Pri PWM, CH3: Syn DRV)



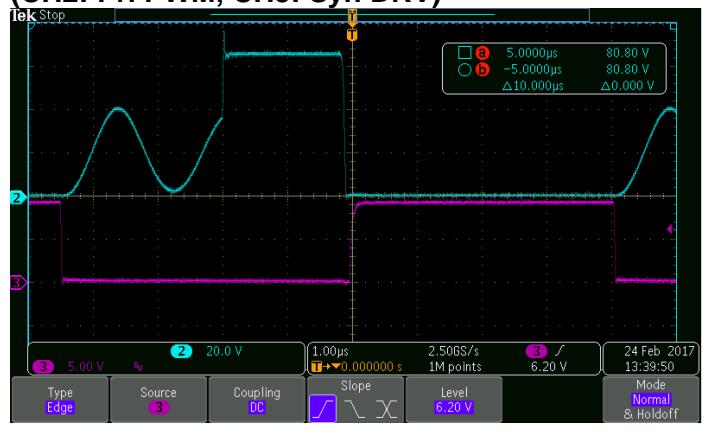
230 Vac input, 15V3A output  
(CH2: Pri PWM, CH3: Syn DRV)



115 Vac input, 20V2.25A output  
(CH2: Pri PWM, CH3: Syn DRV)

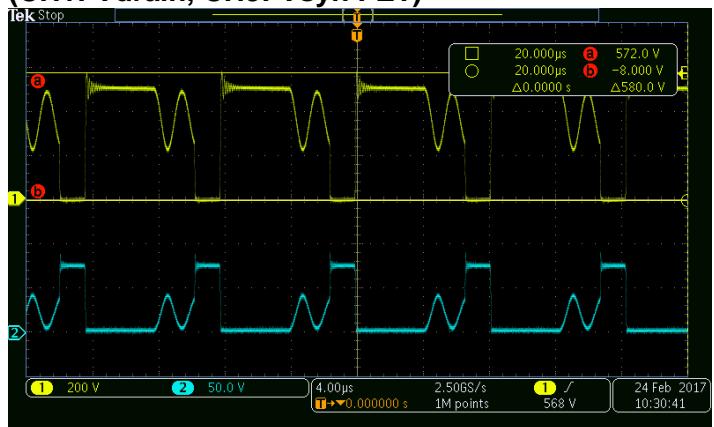


230 Vac input, 20V2.25A output  
(CH2: Pri PWM, CH3: Syn DRV)



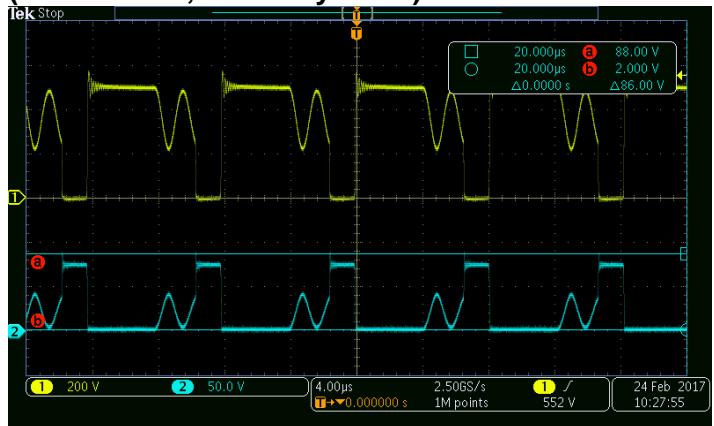
## Primary FET Drain Voltage @ 264 Vax input, 20V2.25A output

(CH1: V<sub>drain</sub>, CH3: V<sub>syn</sub> FET)

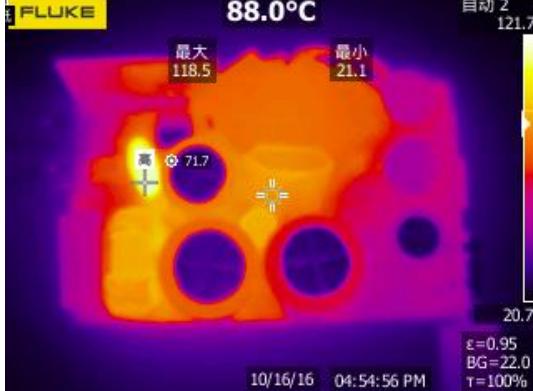
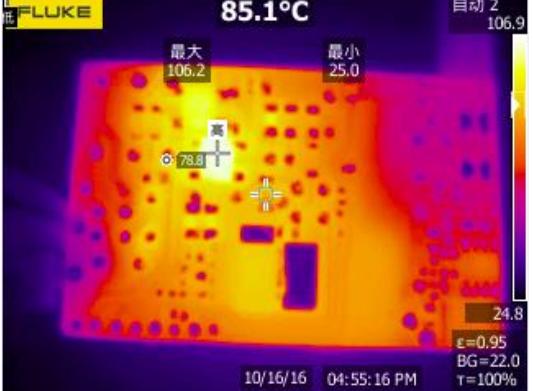
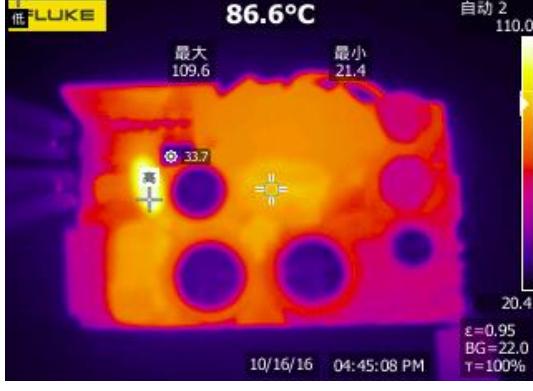
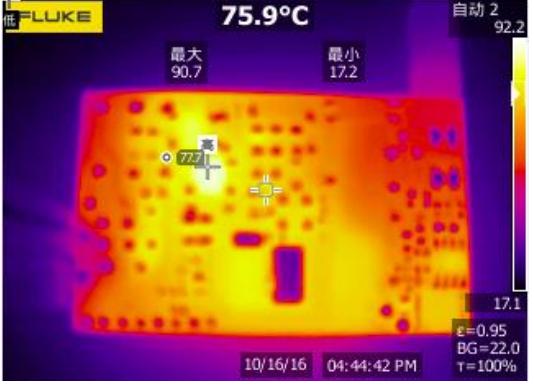
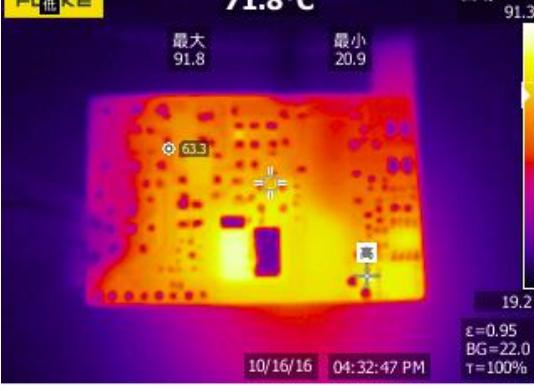
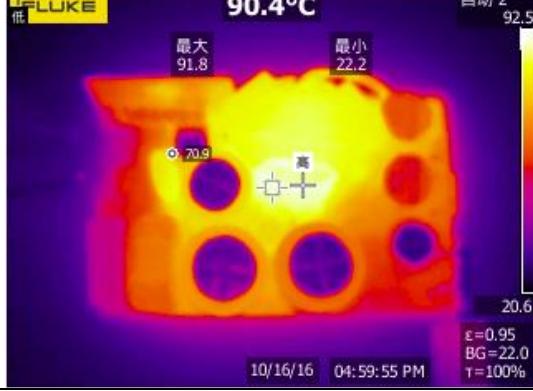
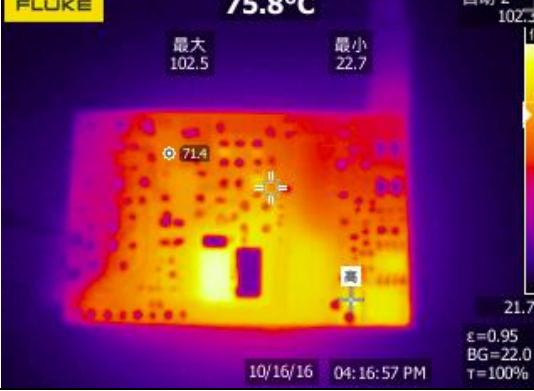


## Synchronic FET Drain Voltage @ 264 Vax input, 20V2.25A output

(CH1: V<sub>drain</sub>, CH3: V<sub>syn</sub> FET)



## Thermal Image @ 20V2.25A Output

Input	Component Side	Back side
90 Vac	 <p>FLUKE 88.0°C 自動 2 121.7 最大 118.5 最小 21.1 高 10/16/16 04:54:56 PM</p>	 <p>FLUKE 85.1°C 自動 2 106.9 最大 106.2 最小 25.0 高 10/16/16 04:55:16 PM</p>
115 Vac	 <p>FLUKE 86.6°C 自動 2 110.0 最大 109.6 最小 21.4 高 10/16/16 04:45:08 PM</p>	 <p>FLUKE 75.9°C 自動 2 92.2 最大 90.7 最小 17.2 高 10/16/16 04:44:42 PM</p>
230 Vac	 <p>FLUKE 84.9°C 自動 2 90.9 最大 89.2 最小 20.0 高 10/16/16 04:32:26 PM</p>	 <p>FLUKE 71.8°C 自動 2 91.3 最大 91.8 最小 20.9 高 10/16/16 04:32:47 PM</p>
264 Vac	 <p>FLUKE 90.4°C 自動 2 92.5 最大 91.8 最小 22.2 高 10/16/16 04:59:55 PM</p>	 <p>FLUKE 75.8°C 自動 2 102.3 最大 102.5 最小 22.7 高 10/16/16 04:16:57 PM</p>

## DN05100/D

## BOM

Item	Qty	Reference	Type	Part Name	MFR	Value	Package	Description
1	1	C11	Ceramic Capacitor	std	std	0.1uF	603	Capacitor, Ceramic, 50V, 10%
2	1	C9	Ceramic Capacitor	Std	std	102	603	Capacitor, Ceramic, 50V, 10%
3	1	C26	Ceramic Capacitor	C3216X7T2W104K	TDK	104, 400V	1206	Capacitor, Ceramic, SMD, 5%
4	1	C19	Ceramic Capacitor	Std	std	10nF	603	Capacitor, Ceramic, 50V, 10%
5	1	C12	Ceramic Capacitor	C1608C0G2A102J	TDK	1nF, 100v	603	Capacitor, Ceramic, SMD, 5%
6	5	C15-17 C24 C27	Ceramic Capacitor	C1608X7R1E105K	TDK	1uF, 25V	603	Capacitor, Ceramic, 25V, 10%
7	1	C8	Ceramic Capacitor	C3216X7S2A225K	TDK	2.2uF, 100v	1206	Capacitor, Ceramic, 100V, 10%
8	1	C21	Ceramic Capacitor	C3216X7S2A225K	TDK	2.2uF, 100v	1206	Capacitor, Ceramic, 100V, 10%
9	1	C4	Ceramic Capacitor	C2012X7R1V475K	TDK	4.7uF, 35v	805	Capacitor, Ceramic, 35V, 10%
10	1	C3	Ceramic Capacitor	C3216C0G2J471J	TDK	470pF, 630V	1206	Capacitor, Ceramic, Chip, 5%
11	1	C10	Ceramic Capacitor	CS65-B2GA101KYNKA	TDK	470pF, Y1	Lead type	HV Ceramic, safety standard approved, 10%
12	2	C22-23	Ceramic Capacitor	Std	std	47P	603	Capacitor, Ceramic, 50V, 10%
13	1	C6	Ceramic Capacitor	Std	std	47p	603	Capacitor, Ceramic, 50V, 10%
14	1	C25	X2 Capacitor	/890334022017	Wueth	683, X2	THT, 7.5mm	X2 capacitor, Safety standard approved, 10%
15	2	C18 C20	Ceramic Capacitor	Std	std	NC	603	Capacitor, Ceramic, 50V, 10%
16	1	U5	PD controller	CY2211	CanYon		TSSOP16	CanYon PD protocol controller
17	1	D1//D3	Bridge rectifier	MDB6S	FSC	1A, 600V	Micro-DIP	Bridge Rectifier, 600V, 1A
18	1	DNR	Varistor	820573011	Wurth	10D471K	TH	Varistor, 10D471K
19	1	D6	Switching diode	BAS21HT1G	Vishay	0.2A, 250V	SOD323	Switching diode, SMD
20	4	D7 D10-11 D16	Switching diode	BAS21HT1G	ON	0.2A, 250V	SOD323	Switching diode, SMD
21	1	D4	Switching diode	BAT54HT1G	ON	0.2A, 30V	SOD323	Switching diode, SMD
22	1	D5	Ultrafast rectifier	US1JFA	ON(FSC)	0.8A, 600V	SOD123FL	Standard Rectifier, 0.8A, 600V
23	2	D12-13	Standard rectifier	RS1JFA	ON(FSC)	0.8A, 600V	SOD123FL	Standard Rectifier, 0.8A, 600V
24	2	D2 D14	Switching diode	BAS21HT1G	ON	NC	SOD323	Switching diode, SMD
25	1	D8	Switching diode	BAS21HT1G	ON	0.2A, 250V	SOD323	Switching diode, SMD
26	1	FB	Ferrite bead	UPZ2012E102-1R5TF	Sunlord/Wueth	805		1000ohm@100MHz
27	1	FB1	Ferrite bead	UPZ2012E601-2R0TF	Sunlord/Wueth	805		600ohm@100MHz
28	1	L3	Common filter	744821110	Wueth	10mH	TH type	CM Filter, T type core
29	1	L1	Common filter	150-1327	Wurth-Mic	500uH	TH	T type, 6.3x3x3, 11T, 0.2mmx2 in parallel winding
30	1	F1	Fuse	20T-016H	Hollyfus	1.6A, 250Vac	Axial lead	Micro Fuse, 1.6A/250V
31	1	Q4	NPN Transistor	MMBTA06LT1G	ON		SOT23	General NPN Transistor, SMD

**DN05100/D**  
**BOM (Continued)**

Item	Qty	Reference	Type	Part Name	MFR	Value	Package	Description
32	2	Q6-7	NPN Transistor	MMBT3904LT1G	ON		SOT23	General NPN Transistor, SMD
33	1	Q3	NPN Transistor	MMBTA06LT1G	ON	NC	SOT23	General NPN Transistor, SMD
34	1	Q5	PNP Transistor	MMBT3906LT1	ON		SOT23	GENERAL PURPOSE PNP SILICON TRANSISTOR
35	1	U3	Syn. rectified co	NCP43080DDR2G	ON		S08	Syn. Rectified Controller
36	1	U1	PWM Controller	NCP1340B3D1R2G	ON		SOP9	PWM controller
37	1	NTC1	NTC	std	std	13k	603	13k 0603 resistor as a replacement
38	1	NTC	NTC	SPNL09D2R5MBI	Sunlord	2.5ohm	lead type 9mm Die,	2.5ohm
39	1	U4	Optical coupler	FODM1009	ON(FSC)		LSOP4	optical coupler, standard SOP package
40	1	Q8	PMOS	ATP104-TL-H	ON	-30V, 8.4mA	ATPAK	PMOS
41	1	L2	Axial leaded fixed	7447462470	Wurth	47uH		Axial leaded fixed inductor
42	1	Q2	MOSFET	IPL60R385CP	Infineon		THINKPAK	MOSFET, NChan, 600V
43	1	R6	Resistor	Std	Std	1	603	Resistor, Chip, 1/8W, 1%
44	1	R13	Resistor	Std	Std	100K	603	Resistor, Chip, 1/8W, 1%
45	1	R7	Resistor	Std	Std	100k	603	Resistor, Chip, 1/8W, 1%
46	1	R35	Resistor	Std	Std	10K	603	Resistor, Chip, 1/8W, 1%
47	1	R8	Resistor	Std	Std	10k	603	Resistor, Chip, 1/8W, 1%
48	2	R18 R23	Resistor	Std	Std	10k	603	Resistor, Chip, 1/8W, 1%
49	1	R25	Resistor	Std	Std	150K	603	Resistor, Chip, 1/8W, 1%
50	1	R17	Resistor	Std	Std	18k	603	Resistor, Chip, 1/8W, 1%
51	1	R3	Resistor	Std	Std	1K	603	Resistor, Chip, 1/8W, 1%
52	1	R22	Resistor	Std	Std	1K	603	Resistor, Chip, 1/8W, 1%,
53	1	R9	Resistor	Std	Std	2k	603	Resistor, Chip, 1/8W, 1%
54	1	R11	Resistor	Std	Std	300	603	Resistor, Chip, 1/8W, 1%
55	1	R33	Resistor	Std	Std	39k	603	Resistor, Chip, 1/8W, 1%
56	2	R10 R34	Resistor	Std	Std	4.7	603	Resistor, Chip, 1/8W, 1%
57	1	R15	Resistor	Std	Std	47k	603	Resistor, Chip, 1/8W, 1%
58	1	R32	Resistor	Std	Std	68K	603	Resistor, Chip, 1/8W, 1%
59	1	R20	Resistor	Std	Std	750k	603	Resistor, Chip, 1/8W, 1%
60	1	R16	Resistor	Std	Std	75k	603	Resistor, Chip, 1/8W, 1%
61	1	R19	Resistor	Std	Std	91k	603	Resistor, Chip, 1/8W, 1%
62	1	R24	Resistor	Std	Std	NC	603	Resistor, Chip, 1/8W, 1%

**DN05100/D  
BOM (Continued)**

## **References**

ON Semiconductor datasheet for NCP1340/43080/, NTMFS6B03, ATP104

ON Semiconductor Design Notes DN05043

CanYon semiconductor datasheet for CY2211

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