# AX5043 Sending FM Tones (ZVEI)

## Introduction

This application note describes how to use the AX5043 for transmitting FM tones as they are used for selective calling (ZVEI and similar schemes).

## The AX5043\_TX\_FM\_tones\_(ZVEI) Project

The MASTER/main.c file of the AX5043\_TX\_FM\_tones\_(ZVEI) project demonstrates the transmission of FM tones. Upon pushing button "D" on the DVK-2 mainboard the tones encoding the digits 'A', '5', '0', '4', '3' in the ZVEI scheme are transmitted.

The carrier frequency is 868.3 MHz,  $\pm$ 3 kHz FM deviation are used and an 48 MHz TCXO is assumed.

The FM tones are generated as follows: The AX5043 is set up transmitting CW. Using a TIMER0 interrupt the frequency register is updated periodically in order to generate the desired tone. This is done for a defined number of sampling points. The procedure can be repeated for generating further tones. Finally the AX5043 is put into power-down mode and the MCU goes into sleep mode.

NOTE: This project was derived from an AX-RadioLAB project. However, due to the nature of the task, the dependence of the AXRadioV2API has been removed and communication with the AX5043 radio chip is done directly in the code or via LibMF routines. In order to work with this project MASTER/MASTER.cbp should be opened using the AXCode::Blocks IDE.

The files axradiolabstate.xml and AX\_Radio\_Lab\_output/\* have been kept. Thus it is still possible to open the project using AX-RadioLAB in order to compute register values when attempting to change parameters. However, the files AX\_Radio\_Lab\_output/\* are not used by the code. Changed register values have to be copied manually from AX\_Radio\_Lab\_output/config.c into the init\_ax5043() in main.c (see below).

When using the F143–MINI–DVK rather than the DVK–2 it is necessary to comment in the line "//#define MINI\_KIT" and to comment out "#define USE\_LCD" at the top of main.c

## Routines

• void init ax5043(void)

This routine resets the AX5043, configures the registers necessary for transmit and ranges the PLL for the frequency configured in the FREQA register. In order to change parameters as the carrier frequency, internal vs external VCO, XTAL or TCXO frequency etc. It is easiest to open the



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project with with AX–RadioLAB, change the corresponding parameters and update the register values in init\_ax5043\_regs from the values found in AX\_Radio\_Lab\_output/config.c (the carrier frequency is not assigned to the FREQA register in config.c, but assigned to the axradio\_phy\_chanfreq variable).

When cahnging the carrier frequency it is further necessary to update the freq\_tbl\_sin[][] table (see below).

• void send\_tone\_prepare(void)

This routine powers up the transmitter in CW mode. Further TIMER0 is set up to be clocked at  $f_{radio_xtal}$  / 2^tone\_rclk\_div and to generate interrupts at  $f_s = f_{radio_xtal}$  / 2^tone\_rclk\_div / tone\_t0period. The frequency  $f_s$  is the sampling frequency at which the AX5043 frequency register is updated.

• void send tone(uint8 tn)

This routine sets the frequency of the generated tone by writing the tone\_tbl. It further resets tone\_sample\_cnt, which measures the length of the generated tone in samples. The routine returns after transmitting the tone.

• void send\_tone\_end()

This routine puts the AX5043 into power-down and disables the TIMER0.

• void timer0\_interrupt(void)

This routine toggles the FREQSEL bit in AX5043\_PLLLOOP, which switches between the AX5043\_FREQA and AX5043\_FREQB registers. It then increments the phase accumulator tone\_phase by tone\_freq and writes the frequency of the next sampling point to the currently unused frequency register. Further tone\_cample\_cnt is incremented. This counter measures the length of the tone in samples.

NOTE: The execution time of the timer0\_interrrupt ISR is roughly 10  $\mu$ s. Modifications of this routine, e.g. in order to get rid of the hard-coded carrier frequency, may increase the execution time and thus require lower f<sub>s</sub>. (Increase tone\_rclk\_div or tone\_t0period.)

Variables

• uint8\_t freq\_tbl\_sin[256][4]

This array contains the frequency register values sampling one period of the modulation with 256 points, thus

$$f_{carrier} + f_{deviation} \times sin(2\pi \cdot n/256) \times \frac{2^{24}}{f_{radio\_xta}}$$

For each sampling point the 32bit frequency register value is stored as 4 bytes in the order {FREQA0, FREQA1, FREQA2, FREQA3}. Note that the PLL requires that bit 0 of the FREQA0 is always set in order to prevent tonal behaviour of the Sigma–Delta Modulator.

Note that the full frequency is tabulated, not just the deviation from  $f_{carrier}$ . This avoids 32 bit addition on each frequncy update, allowing maximum sampling speed. The drawback is the lost flexibility in changing  $f_{carrier}$ .

The file MASTER/print\_tab.c contains a simple routine for computing the above table on the AX8052 MCU and printing it on the DebugLink interface.

- uint8\_t tone\_rclk\_div
  - TIMER0 is clocked at  $f_{radio_xtal} / 2^{tone_rclk_div}$ Allowed values are [0, 11].

• uint16\_t tone\_t0period

TIMER0 interrupts for updating the frequency register are generated at the sampling frequency  $f_s = f_{radio\_xtal} / 2^tone\_rclk\_div/tone\_t0period.$ 

Higher  $f_s$  give higher quality representation of the tones. The default used in the example project is  $f_s = 48$  MHz / 2<sup>10</sup> / 1 = 46.875 kHz, which is on the high side.

- NOTE: The execution time of the timer0\_interrrupt ISR, which handles the frequency adjustment, is roughly 10  $\mu$ s. Modifications of that routine, e.g. in order to get rid of the hard-coded carrier frequency, may increase the execution time and thus require lower f<sub>s</sub>.
- const uint16\_t tone\_tbl[]

This array lists the tone frequencies used. Default is ZVEI. The units are  $(f_{tone}/f_s)^*2^{16}$ , where  $f_s$  is the sampling frequency.

• const uint16\_t tone\_len

This variable defines the tone length in samples.

### Conclusion

It is possible to transmit analog FM tones as used in various selective calling schemes using the AX5043.

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