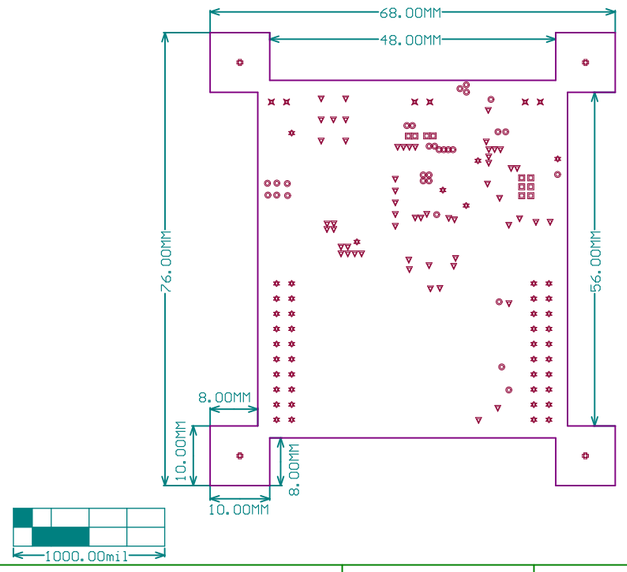
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric 1		8.00mil	4.2	
5	POWER	Copper	1.40mil		
6	Dielectric 2	FR-4 HIGH Tg	40.00mil	4.8	
7	GROUND	Copper	1.40mil		
8	Dielectric 3		8.00mil	4.2	
9	Bottom Layer	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.40mil	3.5	
11	Bottom Overlay				

DESIGN INFORMATION

MIN. TRACK WIDTH: 10 MIL
MIN. CLEARANCE: 7.87 MIL
MIN. VIA PAD SIZE: 19.68 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER _____
THICKNESS: [X] 63 MIL (1.6mm) +/-10% [ ] OTHER _____
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH COPPER THICKNESS: [X] 20-30 um [ ] OTHER _____
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER _____
SOLDER RESIST COLOR: [X] GREEN [ ] OTHER _____
[X] MATTE [ ] SEMI-GLOSS
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____
ARRAY/PANEL: [X] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [ ] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER



Drill Table:

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Via/Pad
	10	7.87mil (0.200mm)	PTH	Round	0.00mil (0.000mm)
	29	10.00mil (0.254mm)	PTH	Round	0.00mil (0.000mm)
	56	12.00mil (0.305mm)	PTH	Round	0.00mil (0.000mm)
	46	40.00mil (1.016mm)	PTH	Round	0.00mil (0.000mm)
	6	43.31mil (1.100mm)	PTH	Round	0.00mil (0.000mm)
	4	125.98mil (3.200mm)	PTH	Round	0.00mil (0.000mm)
	151 Total				

Tolerance:

For 7.874mil via +0/-7.874 mil
For 10mil via +0/-10 mil
For 12mil via +0/-12mil
For PTH drill +/-3mil

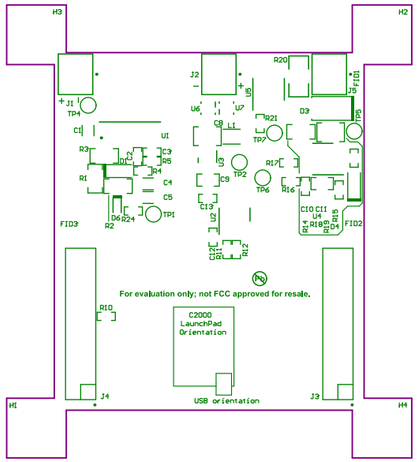
TEXAS INSTRUMENTS

PROJECT TITLE: Implementing Safe brake funtion in VFD
DESIGNED FOR: Public Release
FILE NAME: TIDA-01600.PcbDoc

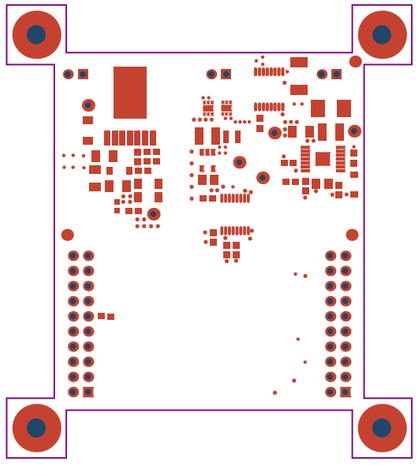
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Fabrication Drawing	GENERATED : 12/11/2017 5:32:37 PM	TEXAS INSTRUMENTS	

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

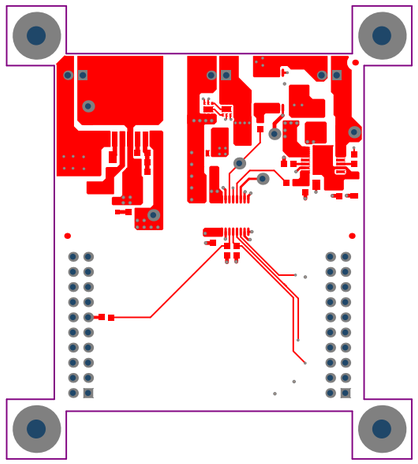
ENGINEER:	LAYOUT BY:
Mega Priya	Koteswara rao
SCALE: 1.00	ALTIUM DESIGNER VERSION: 15.0.15.41991



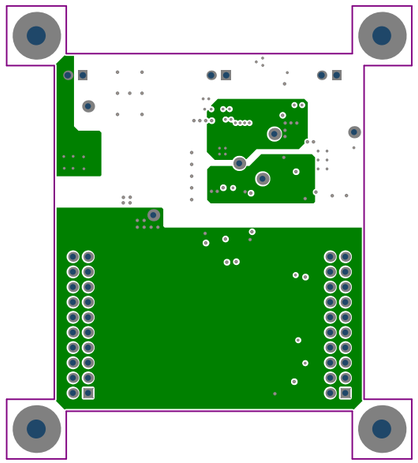
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 12/11/2017 5:25:44 PM		TEXAS INSTRUMENTS



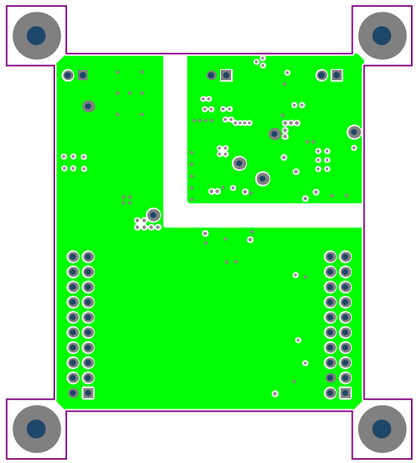
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 12/11/2017 5:25:44 PM		TEXAS INSTRUMENTS



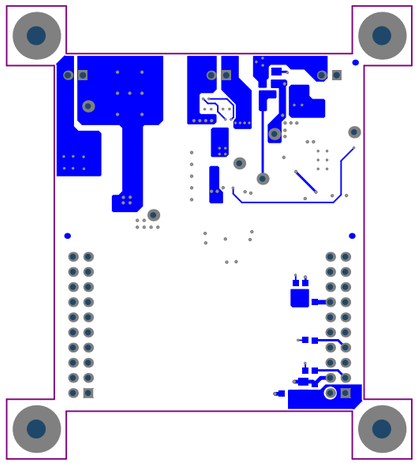
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 12/11/2017 5:25:45 PM		TEXAS INSTRUMENTS



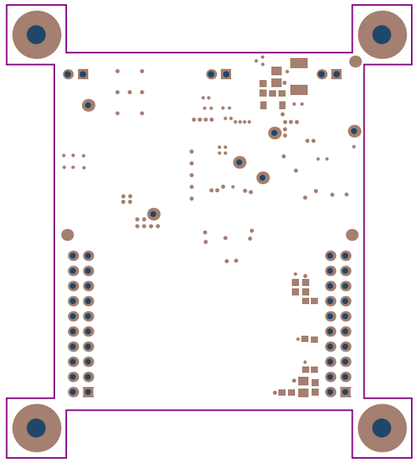
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = POWER	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 12/11/2017 5:25:45 PM		TEXAS INSTRUMENTS



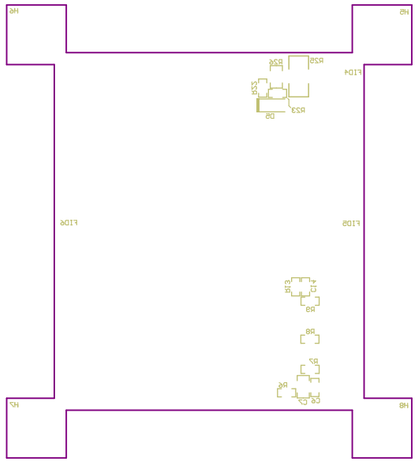
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = GROUND	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 12/11/2017 5:25:46 PM		TEXAS INSTRUMENTS



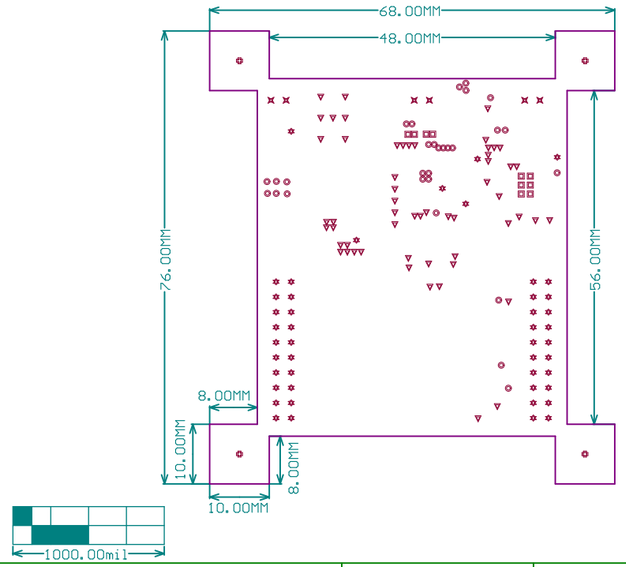
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 12/11/2017 5:25:46 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 12/11/2017 5:25:46 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 12/11/2017 5:25:47 PM		TEXAS INSTRUMENTS



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	4	125.98mil (3.200mm)	PTH	Round	0.00mil (0.000mm)

Tolerance:

For 7.874mil via +0/-7.874 mil
For 10mil via +0/-10 mil
For 12mil via +0/-12mil
For PTH drill +/-3mil

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01600	REV: E1	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 12/11/2017 5:25:47 PM		TEXAS INSTRUMENTS