

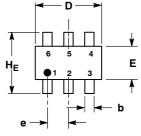


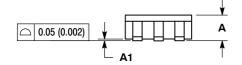
STYLE 1:

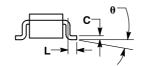
SC-74 CASE 318F-05 **ISSUE N**

DATE 08 JUN 2012

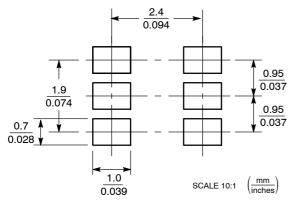








SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLE 3:

STYLE 2:

NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH
 THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
 THICKNESS OF BASE MATERIAL.

 4. 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
С	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	-	10°	0°	_	10°

GENERIC MARKING DIAGRAM*



XXX= Specific Device Code

= Date Code Μ = Pb-Free Package

STYLE 5:

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 6:

PIN 1. CATHODE	PIN 1. NO CONNECTION	PIN 1. EMITTER 1	PIN 1. COLLECTOR 2	PIN 1. CHANNEL 1	PIN 1. CATHODE
2. ANODE	COLLECTOR	2. BASE 1	2. EMITTER 1/EMITTER 2	2. ANODE	ANODE
CATHODE	3. EMITTER	COLLECTOR 2	3. COLLECTOR 1	3. CHANNEL 2	CATHODE
4. CATHODE	4. NO CONNECTION	4. EMITTER 2	4. EMITTER 3	4. CHANNEL 3	4. CATHODE
5. ANODE 6. CATHODE	5. COLLECTOR 6. BASE	5. BASE 2 6. COLLECTOR 1	5. BASE 1/BASE 2/COLLECTOR 3 6. BASE 3	5. CATHODE 6. CHANNEL 4	5. CATHODE 6. CATHODE
O. CATHODE	0. BASE	6. COLLECTOR I	0. BASE 3	0. CHANNEL 4	0. CATHODE
STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:	
PIN 1. SOURCE 1	PIN 1. EMITTER 1	PIN 1. EMITTER 2	PIN 1. ANODE/CATHODE	PIN 1. EMITTER	
2. GATE 1	2. BASE 2	2. BASE 2	2. BASE	2. BASE	_
3. DRAIN 2	3. COLLECTOR 2	3. COLLECTOR 1	3. EMITTER	3. ANODE/CATHODE	=
4. SOURCE 2 5. GATE 2	4. EMITTER 2 5. BASE 1	4. EMITTER 1 5. BASE 1	4. COLLECTOR 5. ANODE	4. ANODE 5. CATHODE	
5. GATE 2 6. DRAIN 1	6. COLLECTOR 1	6. COLLECTOR 2	6. CATHODE	6. COLLECTOR	

STYLE 4:

DOCUMENT NUMBER:	98ASB42973B	Electronic versions are uncontrolled	'	
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document Repository. Printe versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
NEW STANDARD:				
DESCRIPTION:	SC-74		PAGE 1 OF 2	



DOCUMENT	NUMBER:
98ASB42973	R

PAGE 2 OF 2

ISSUE	REVISION	DATE
D	CHANGE OF OWNERSHIP FROM MOTOROLA TO ON SEMICONDUCTOR. DIM A WAS: 2.70-3.10 MM/0.1063-0.1220 IN. DIM C WAS: 1.000-1.30 MM/0.0394-0.0511IN DIM D WAS: 0.25-0.40 MM/0.0098-0.0157 IN. REQ. BY D. TRUHITTE	14 MAR 01
Е	CHANGED "USED ON" WAS: SC-59, 6 LEAD. REQ.BY D. TRUHITTE.	27 MAR 01
F	ADDED STYLE 3. REQ. BY S. BACHMAN.	23 APR 01
G	ADDED STYLE 4. REQ. BY S. BACHMAN.	28 AUG 02
Н	ADDED STYLE 5. REQ. BY B. BLACKMON.	21 OCT 02
J	ADDED STYLE 6. REQ. BY B. BLACKMON.	09 JAN 03
K	ADDED STYLES 7 & 8. REQ. BY S. CHANG	03 JUN 03
L	ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ. BY HONG XIAO.	27 MAY 05
М	ADDED STYLE 9. REQ. BY W. MEADOWS.	11 APR 2006
N	ADDED STYLES 10 & 11. REQ. BY Y. KALDERON.	08 JUN 2012

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